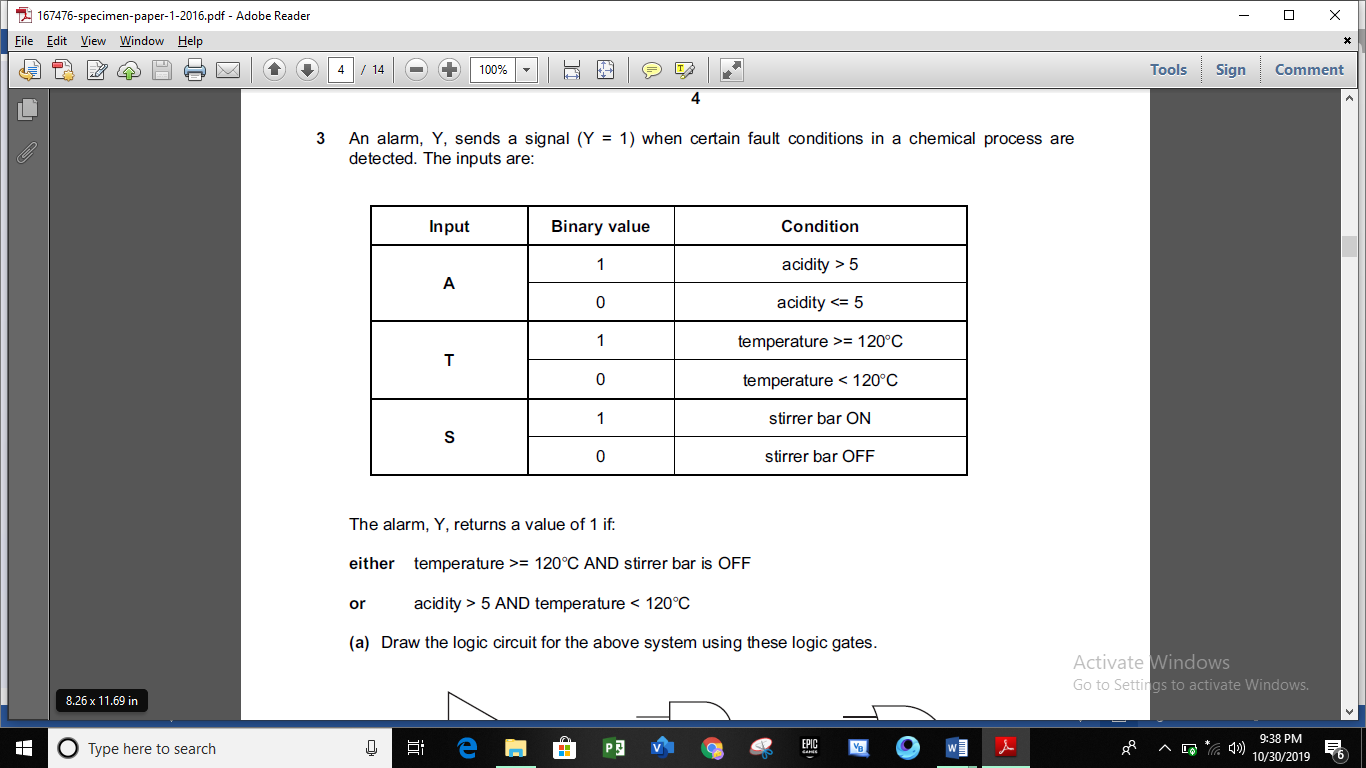
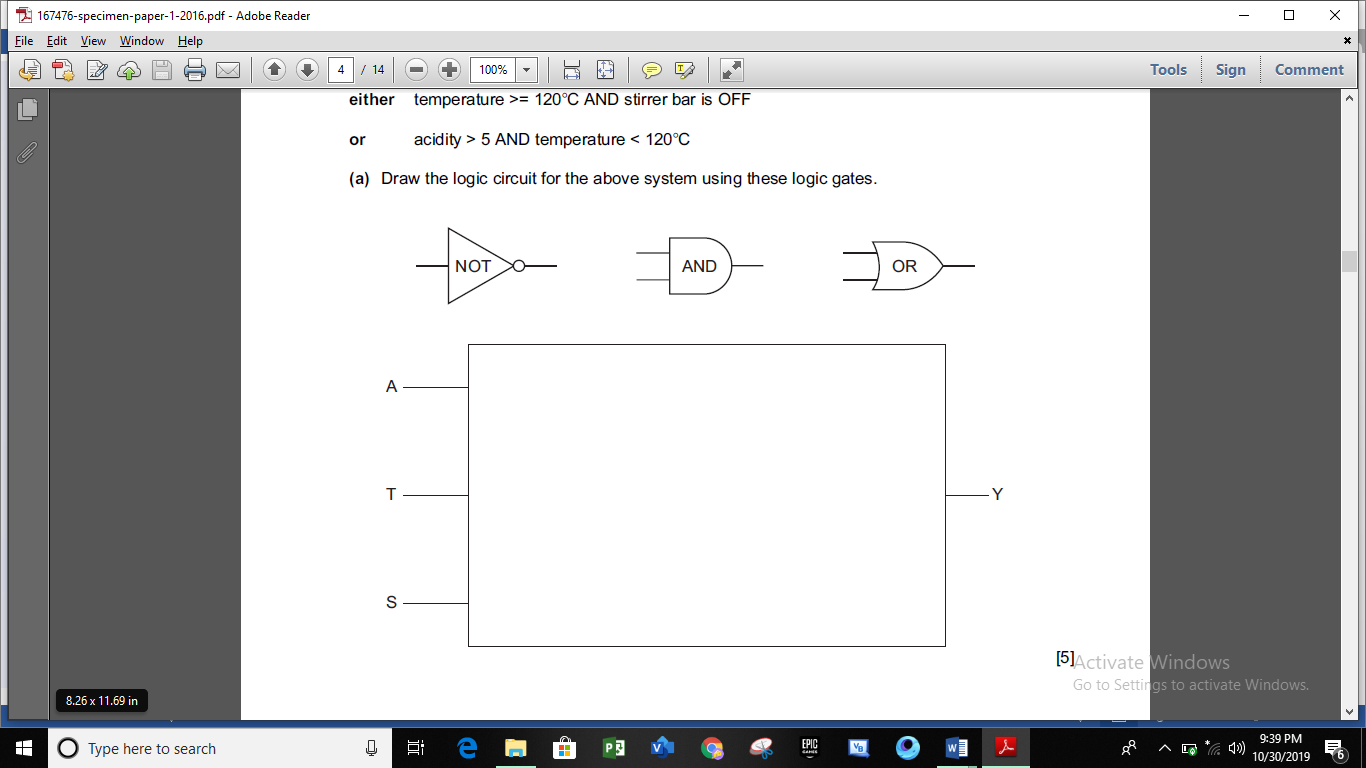
Teacher Name: Lubna Tanweer

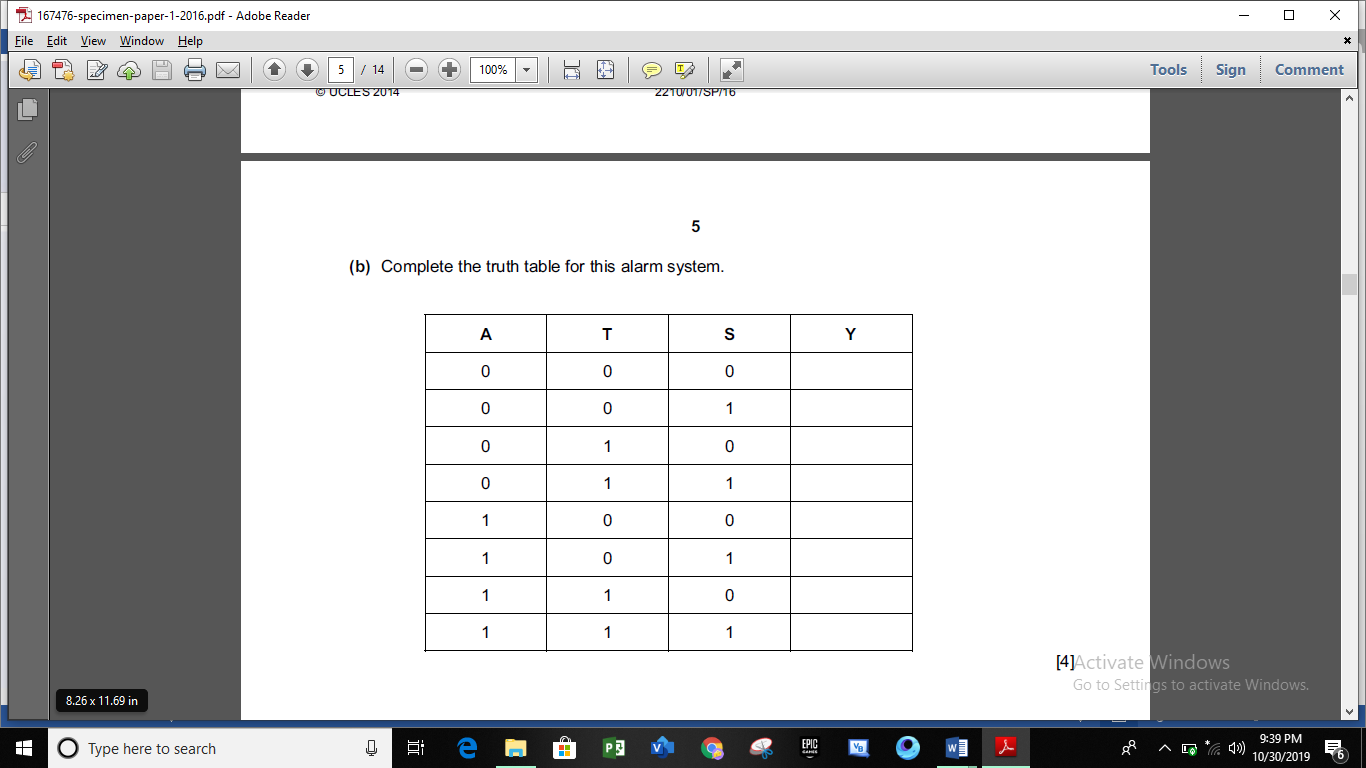
Class: 11 Subject: Computer Science Date: 16th October 2019

Questions from Chapter 3 “Logic Gates and logic circuits”

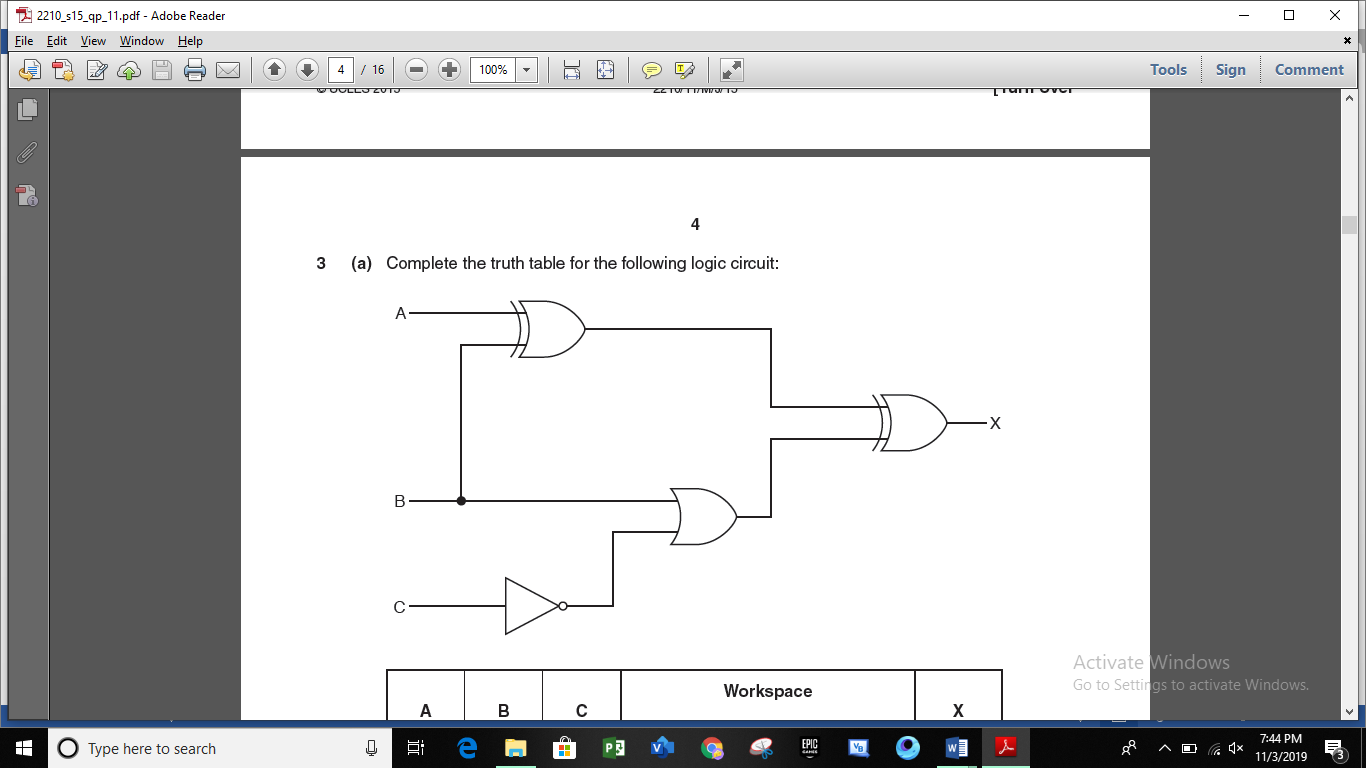
Specimen paper 2015

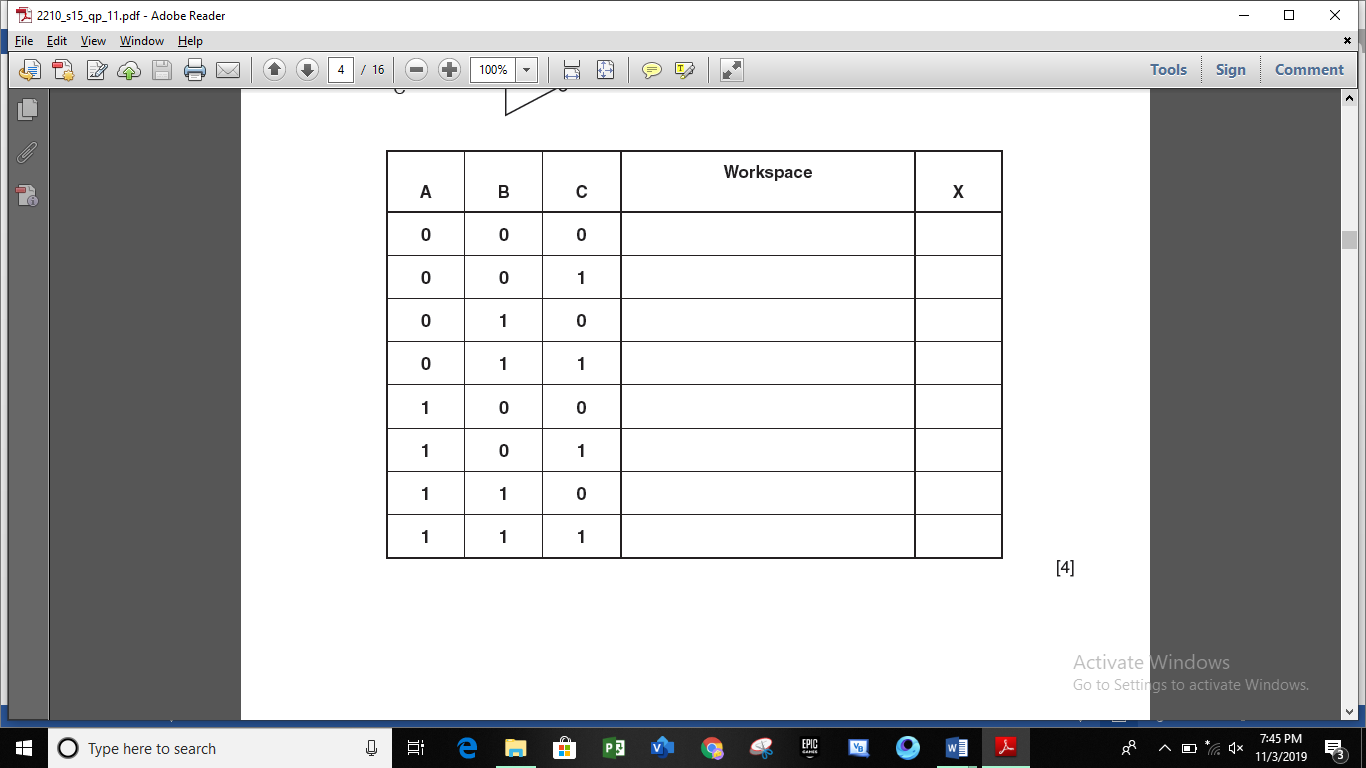


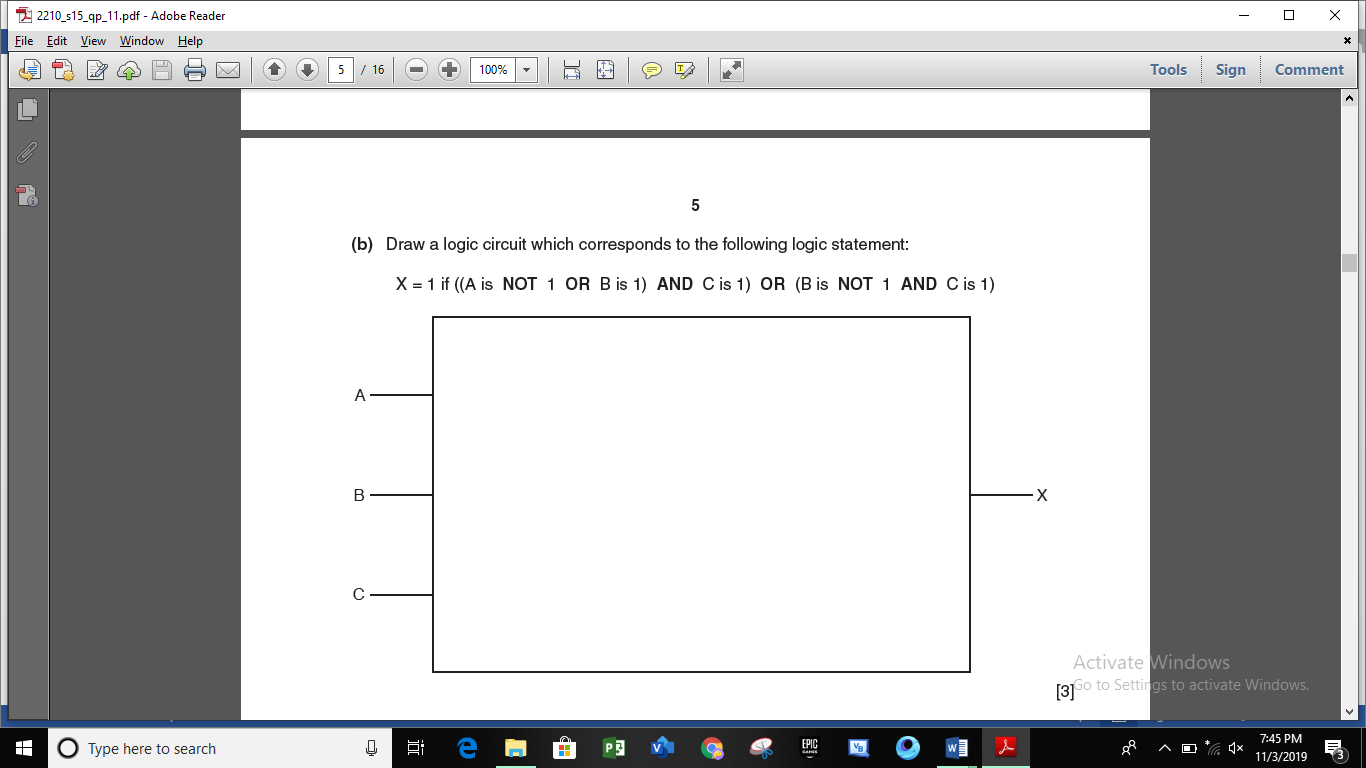


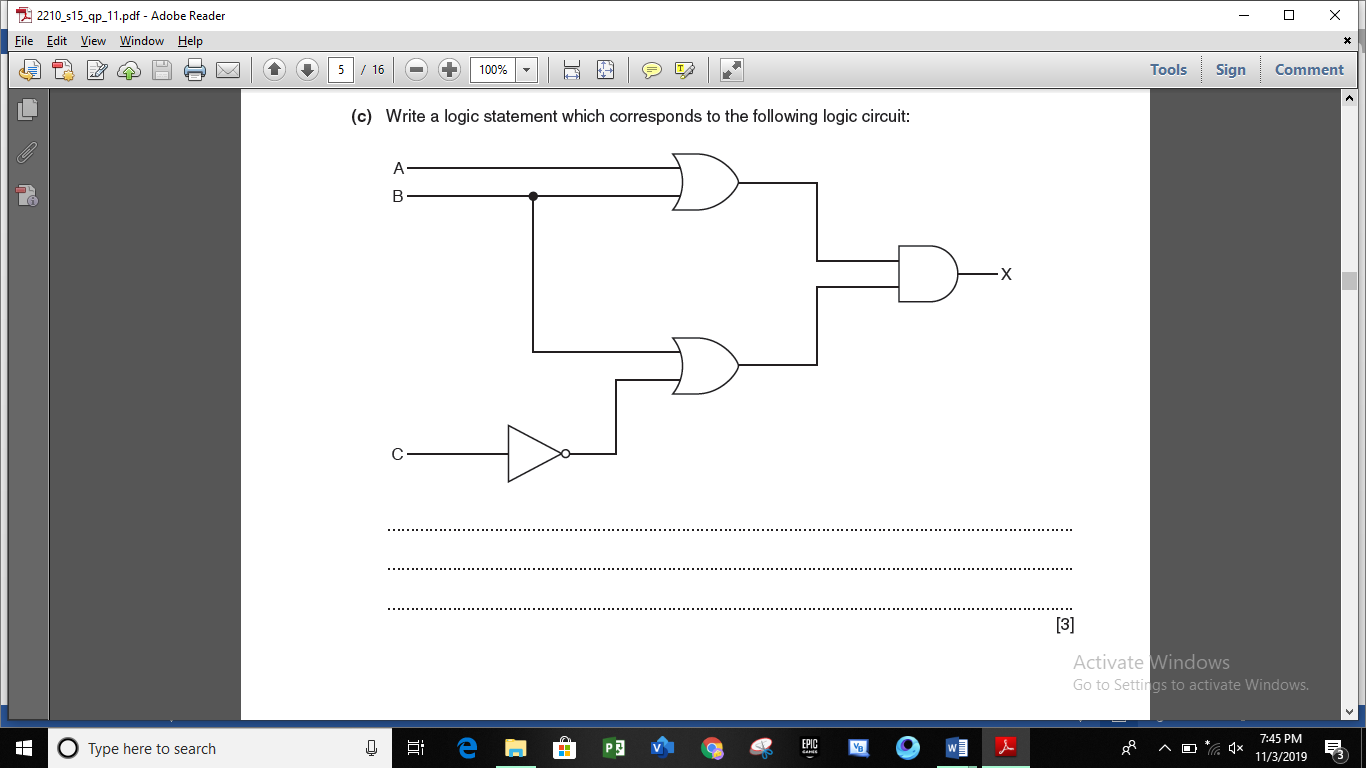


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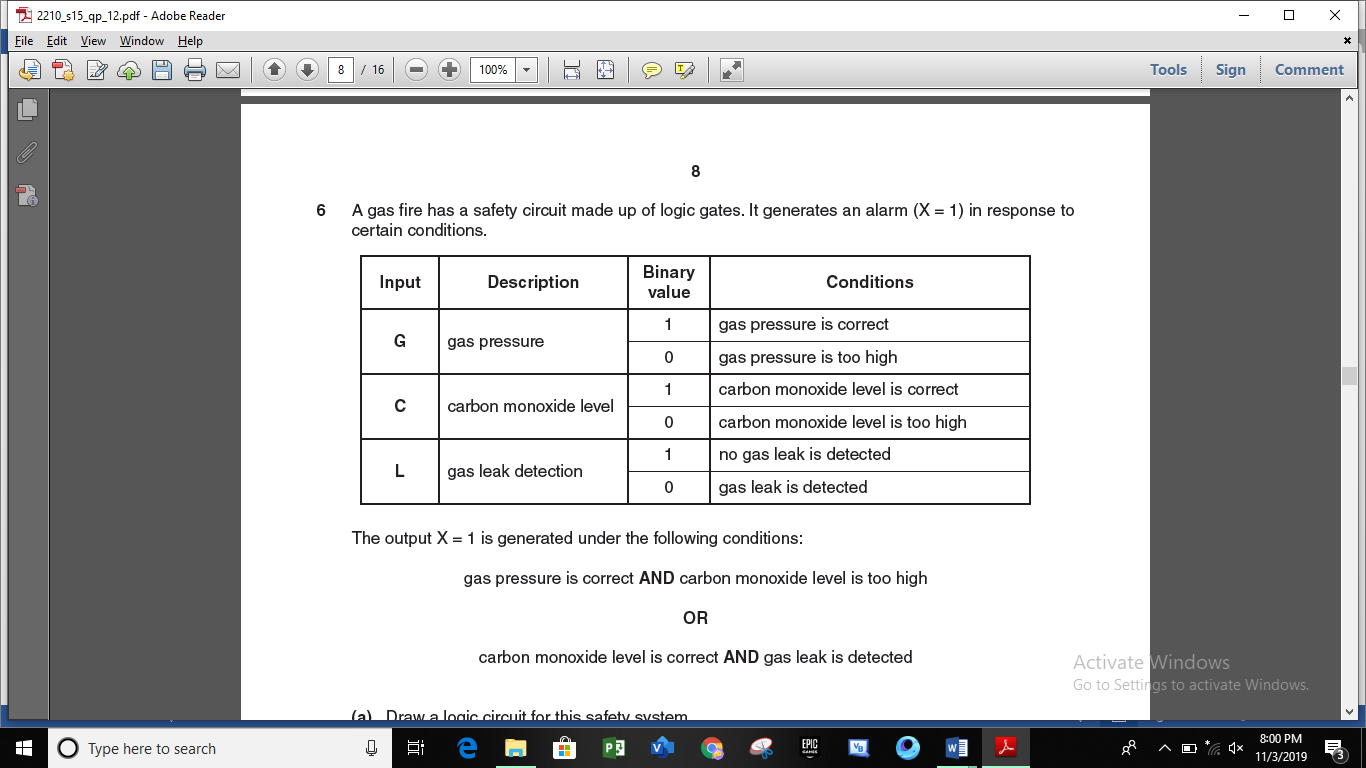
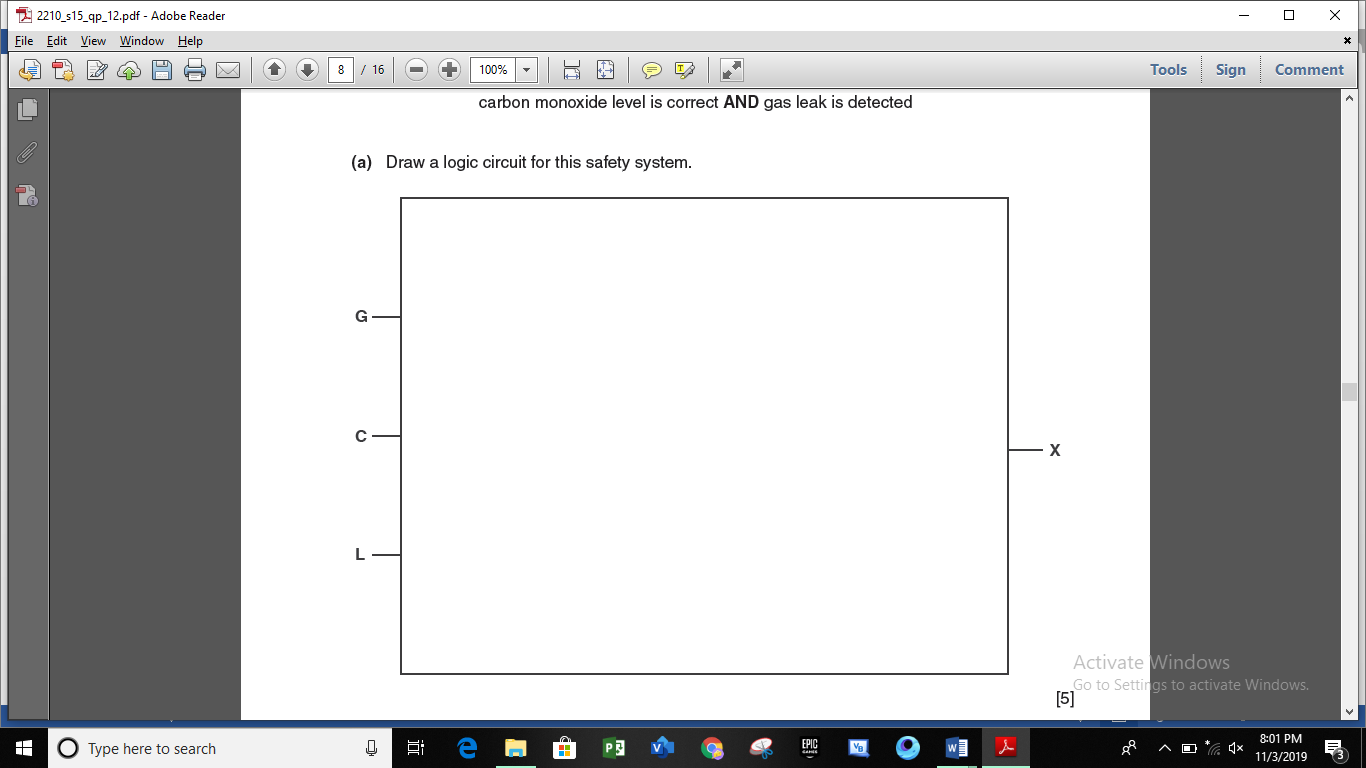
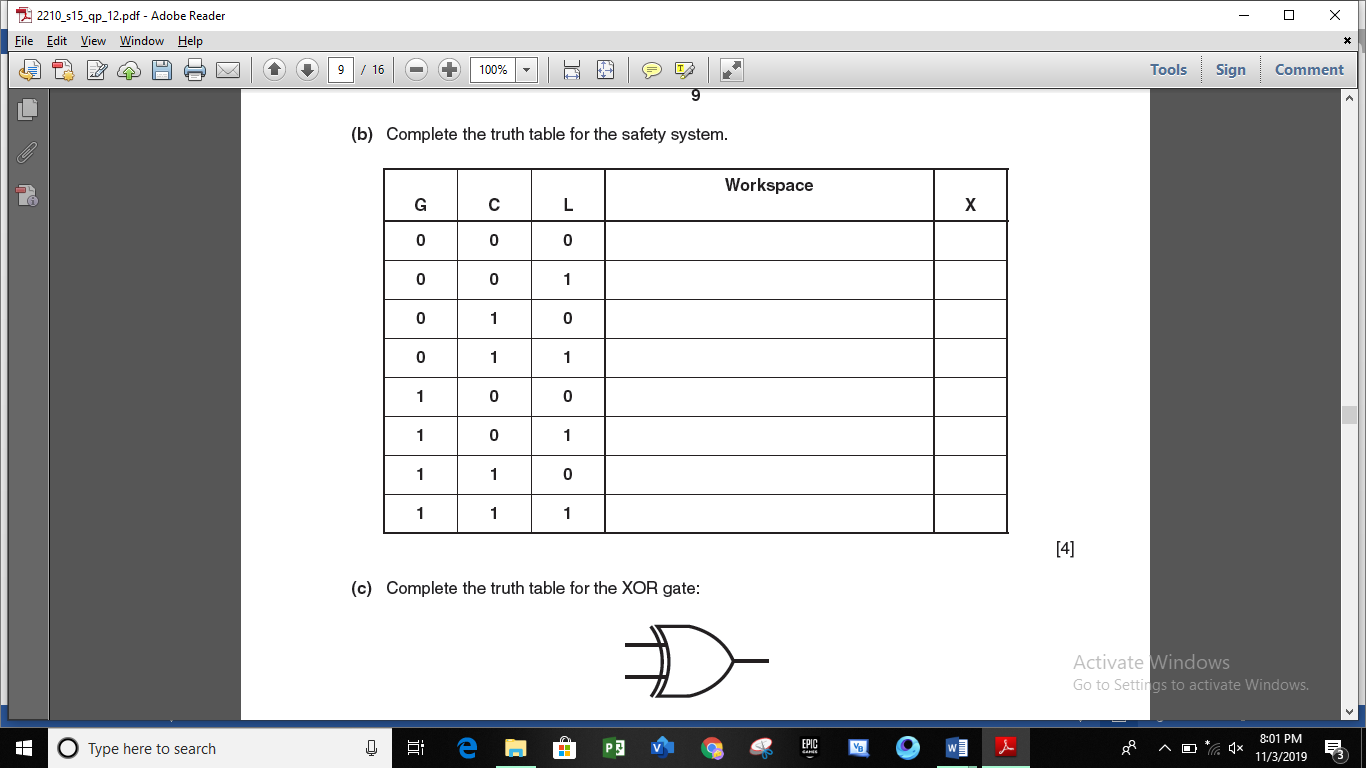
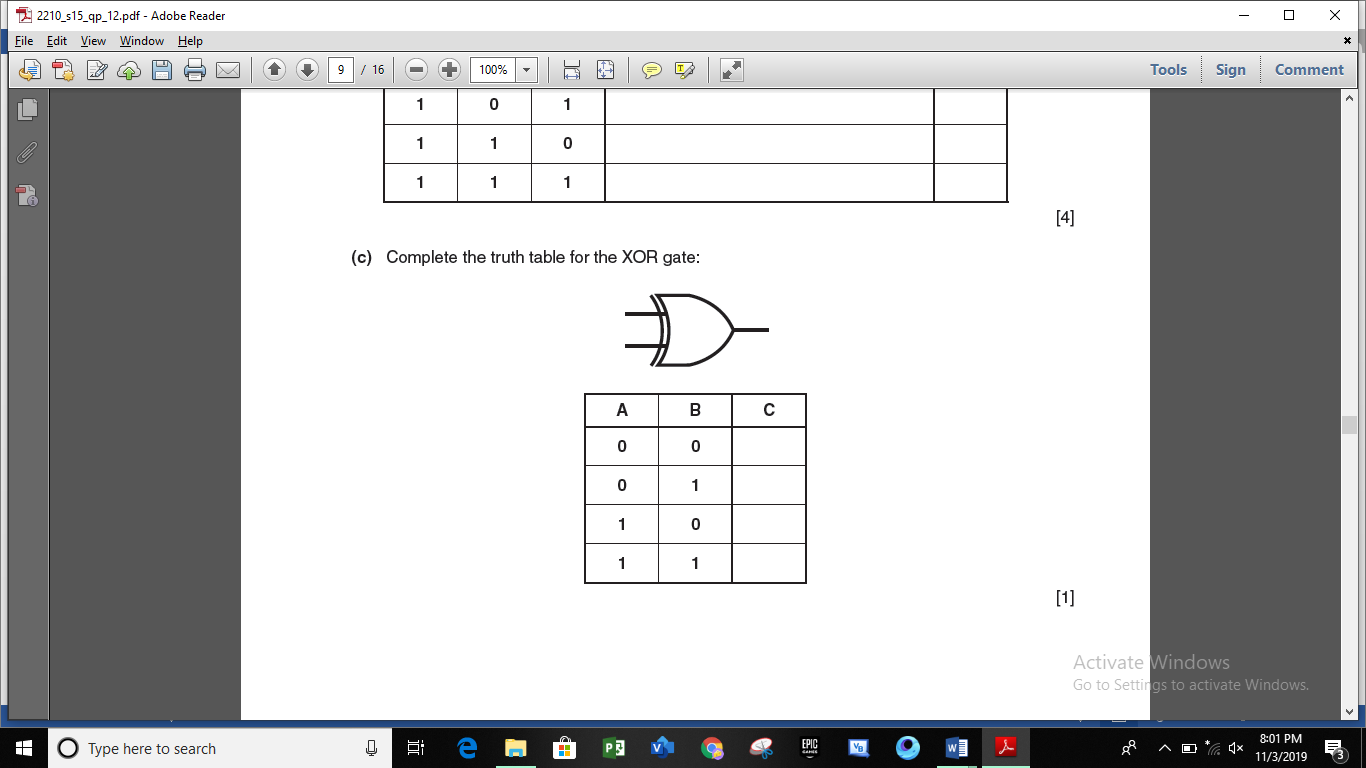








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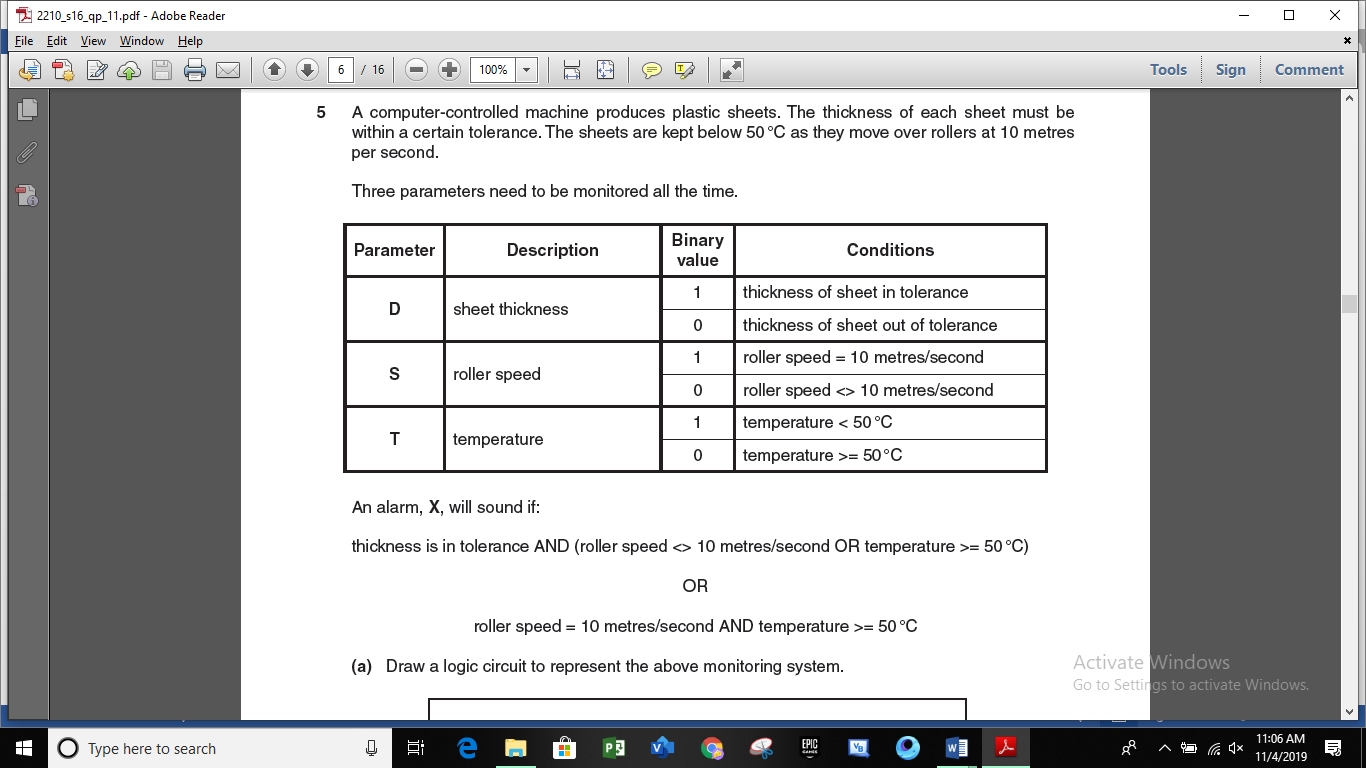
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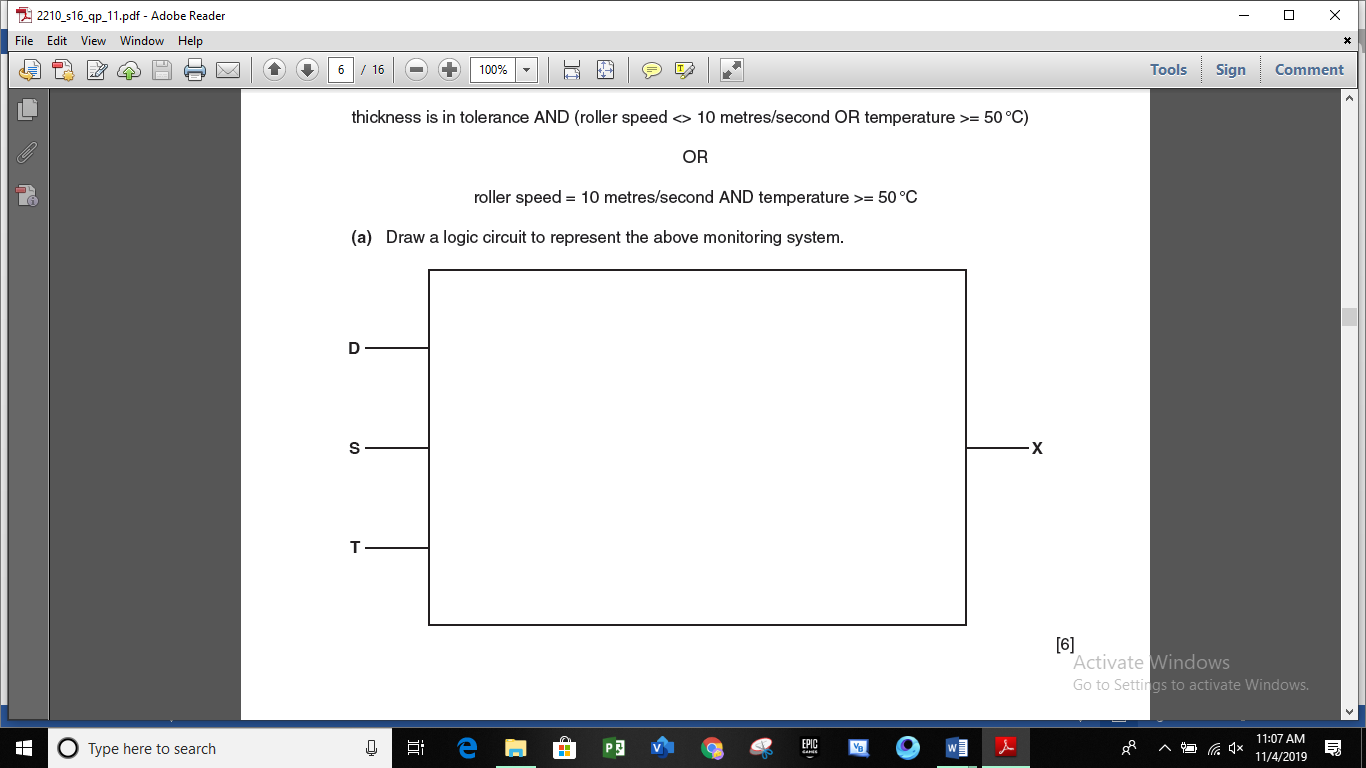
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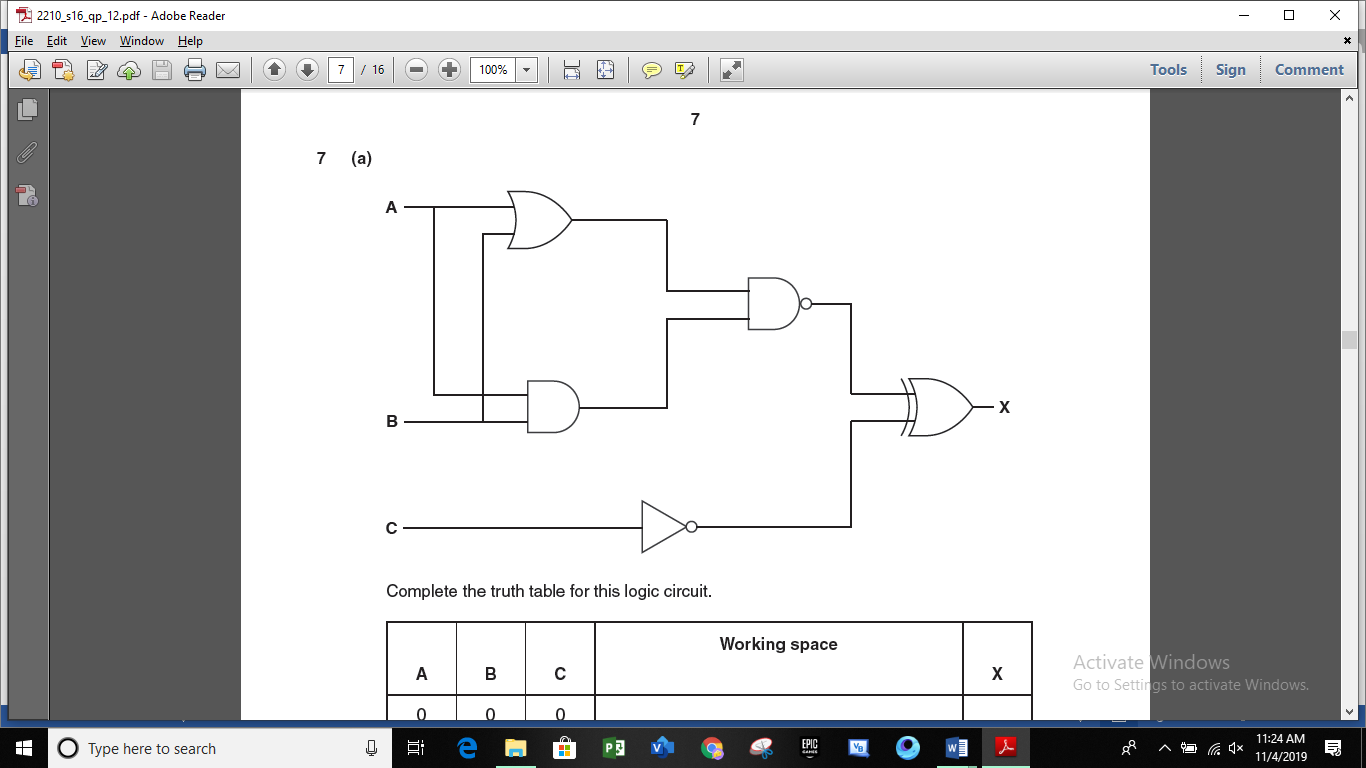
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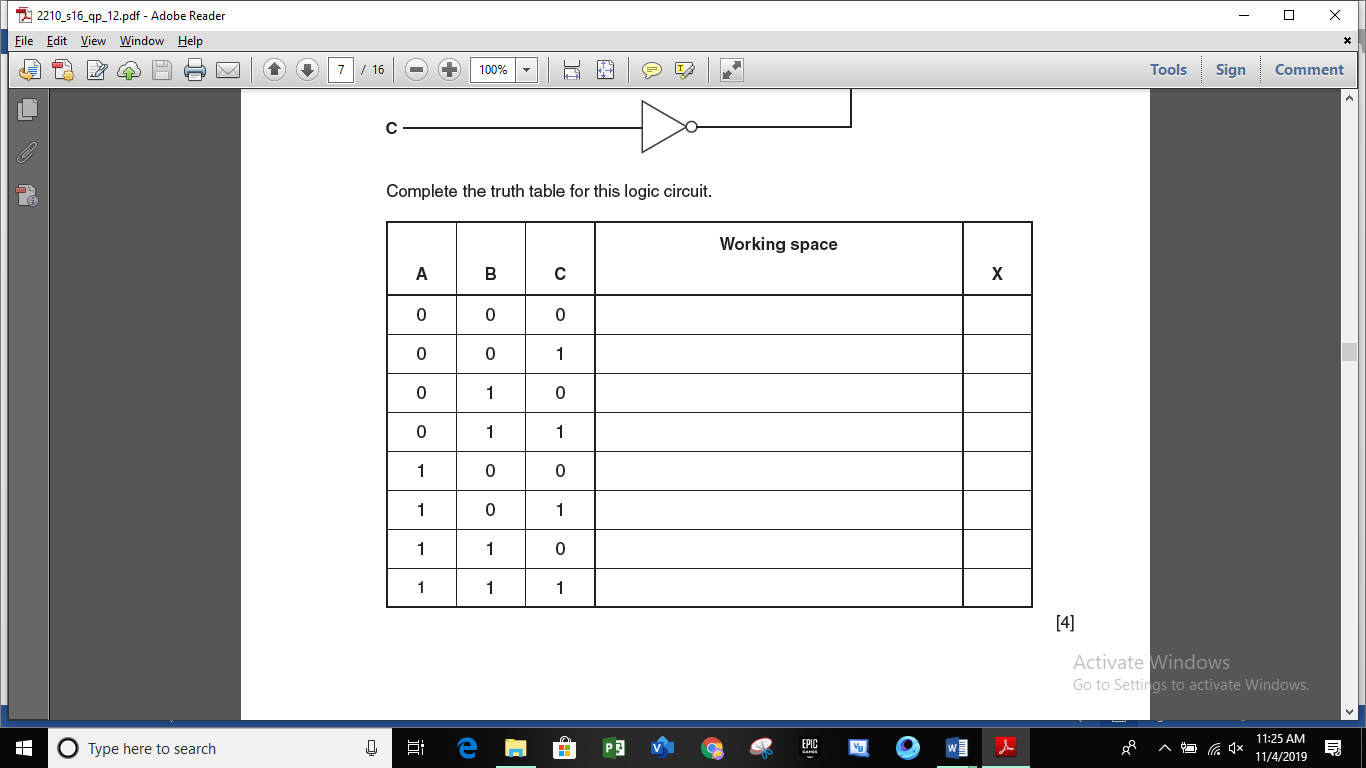


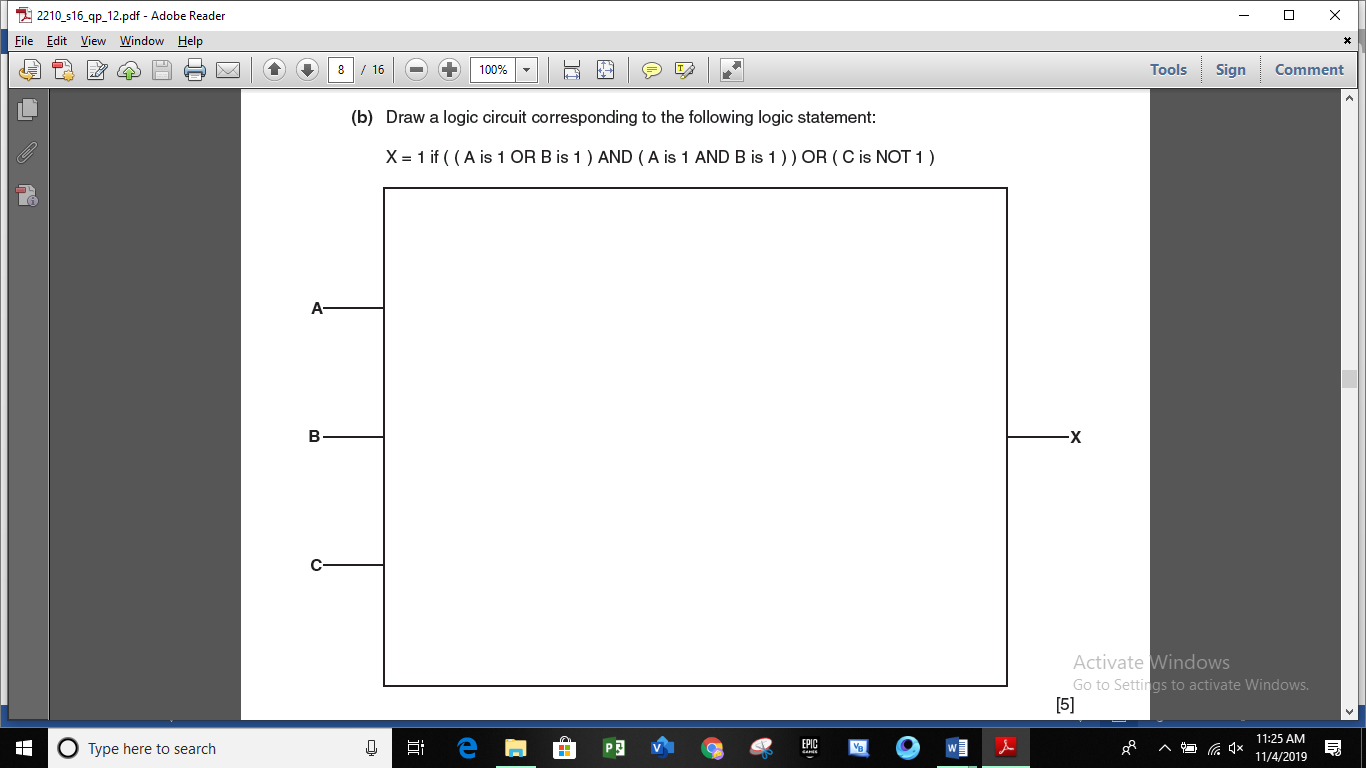


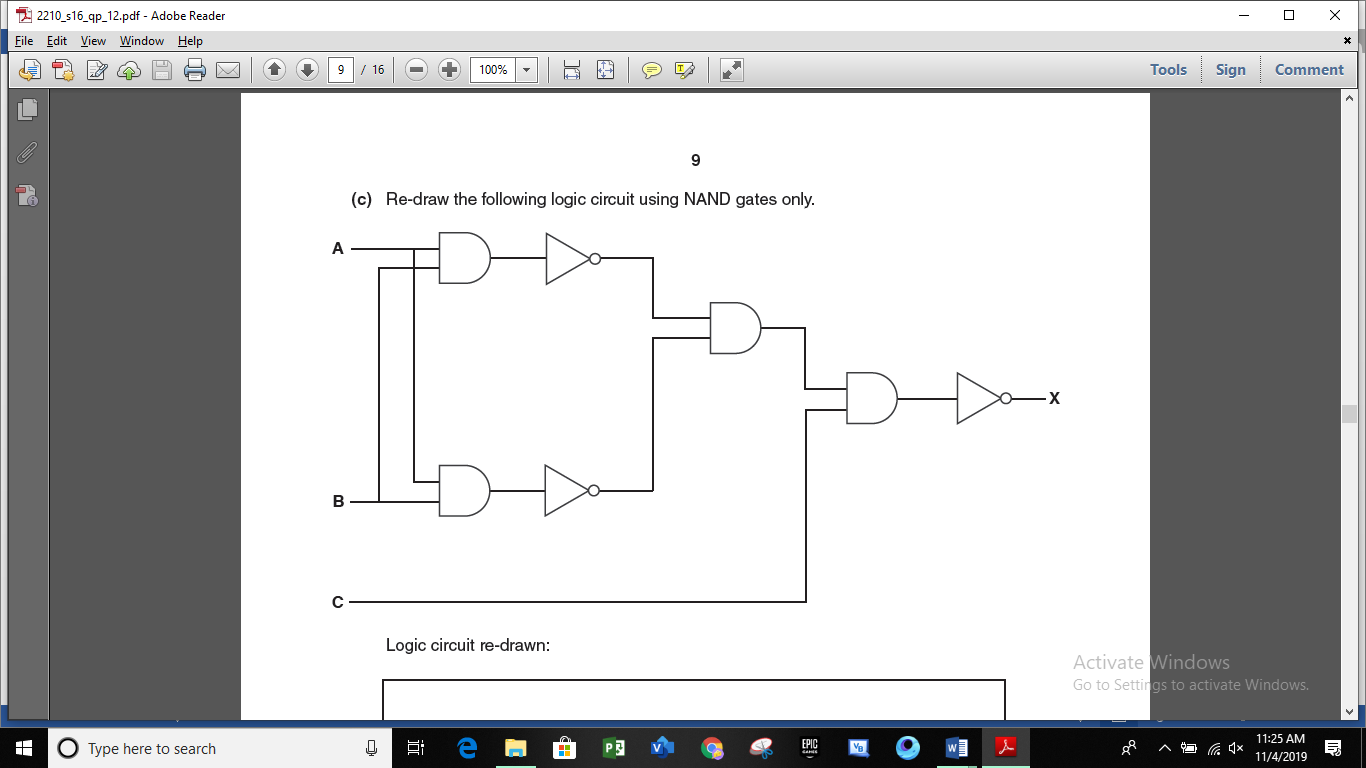


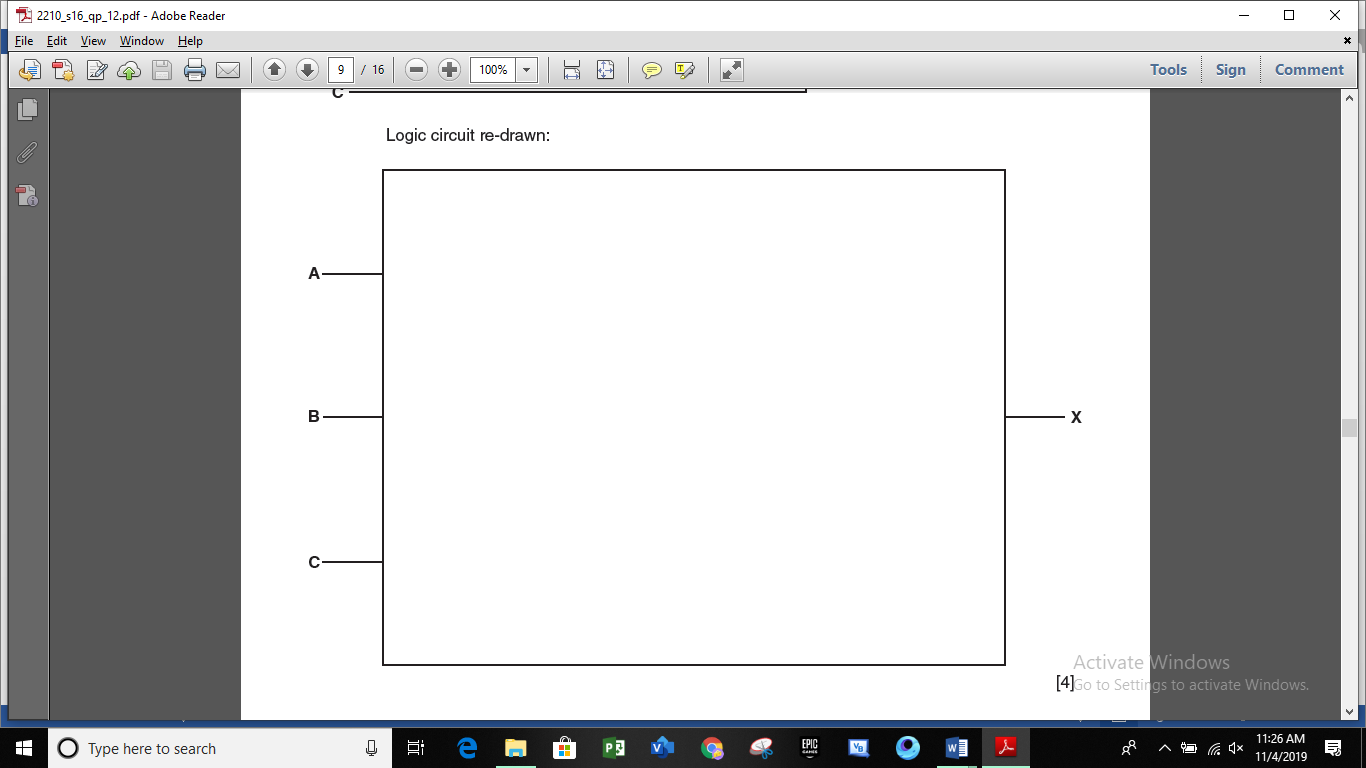
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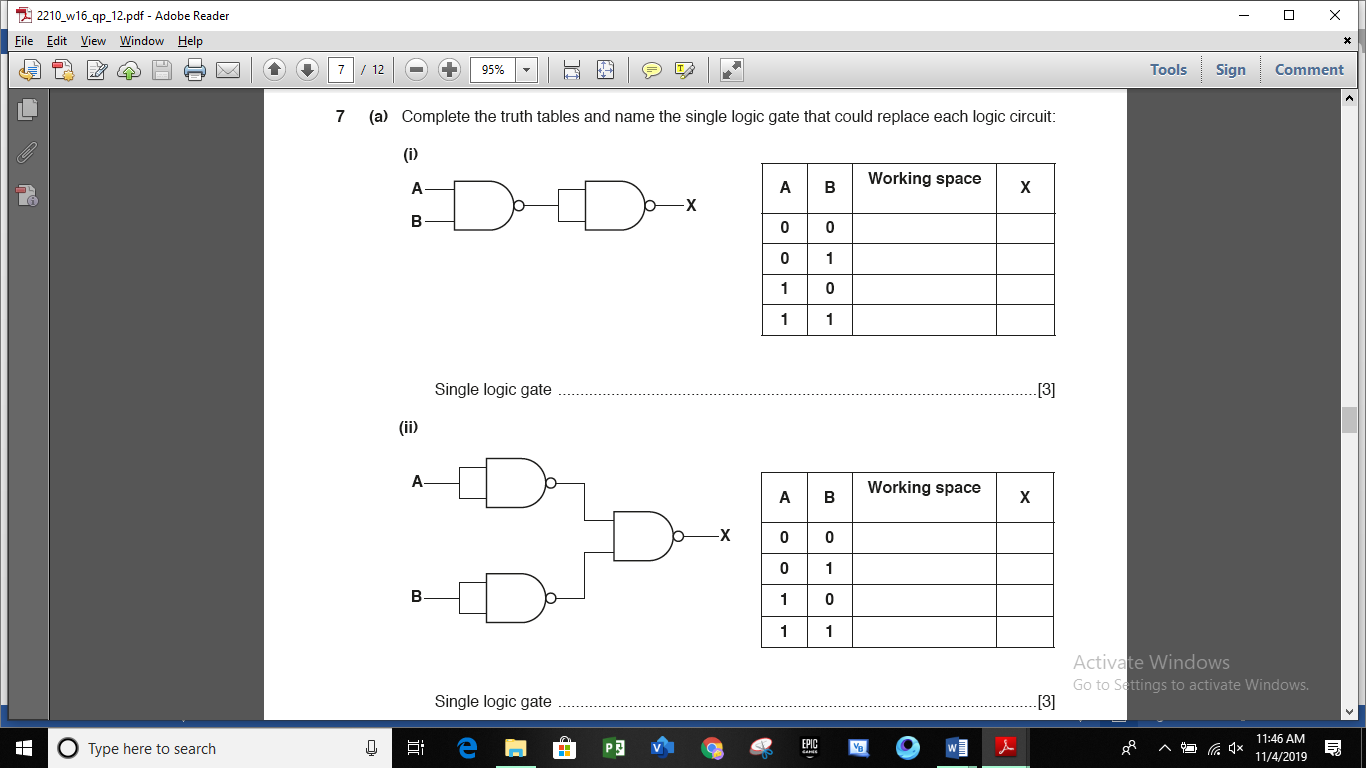


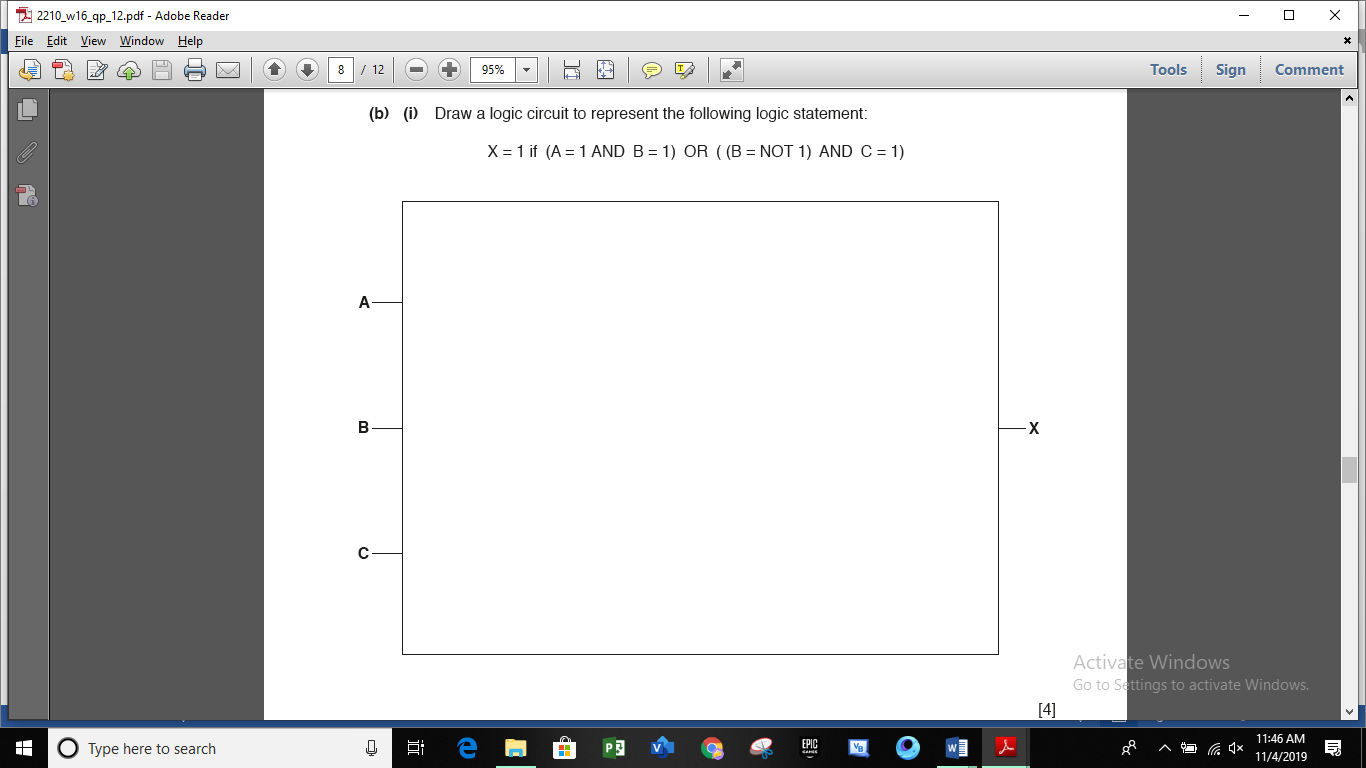


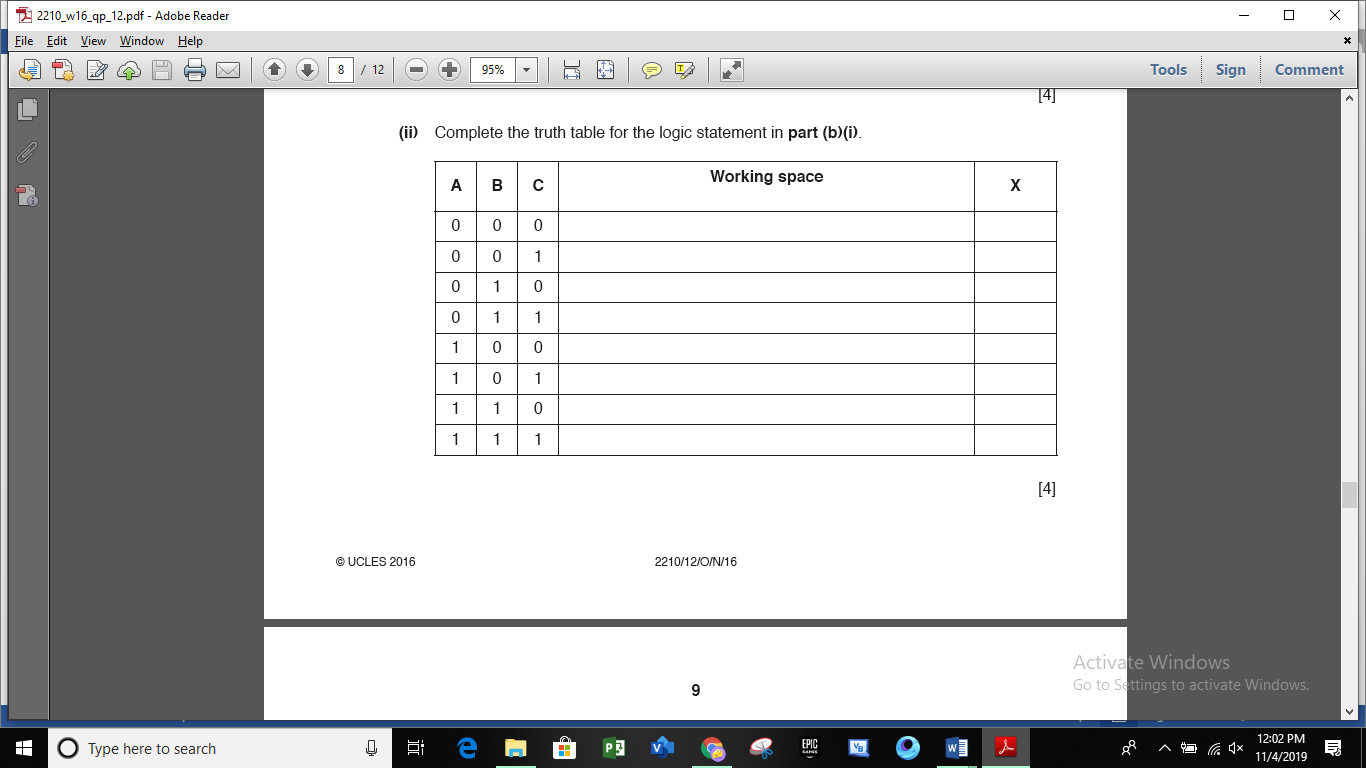




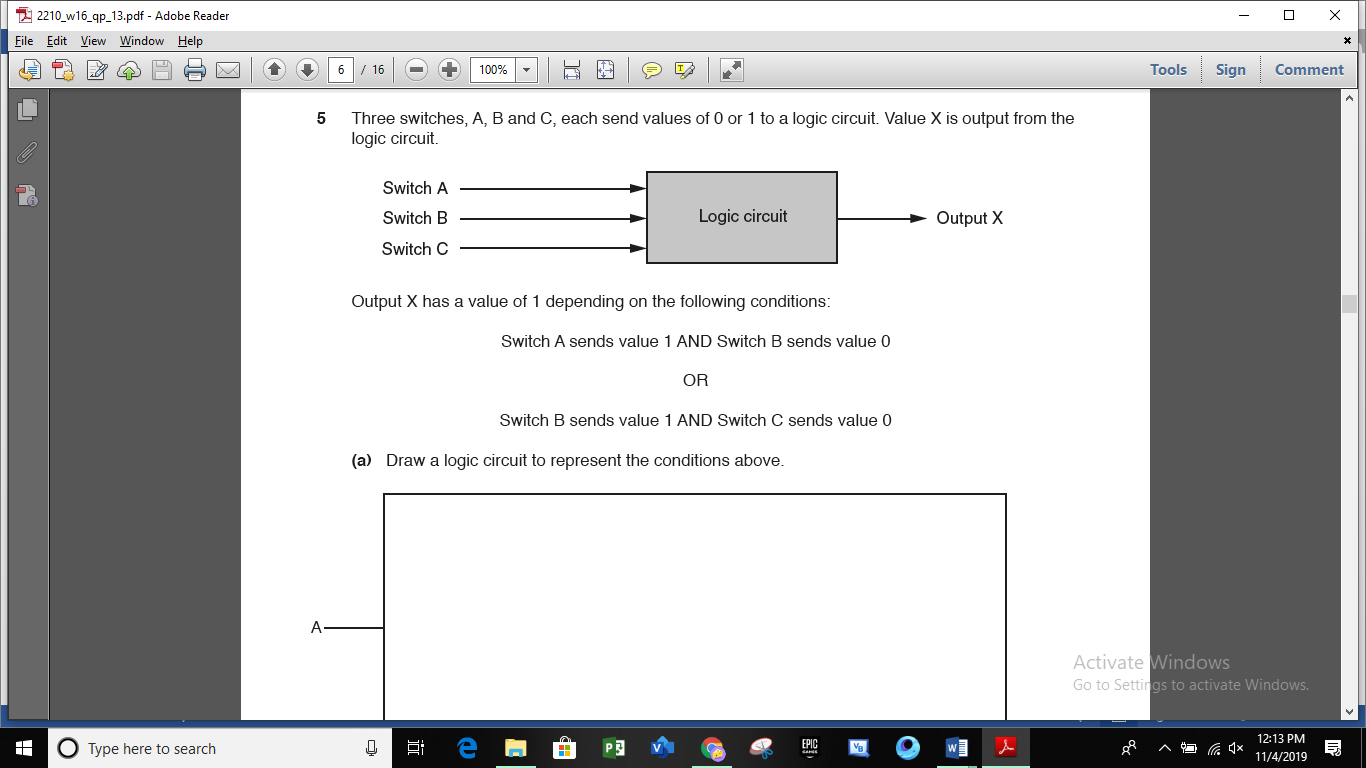
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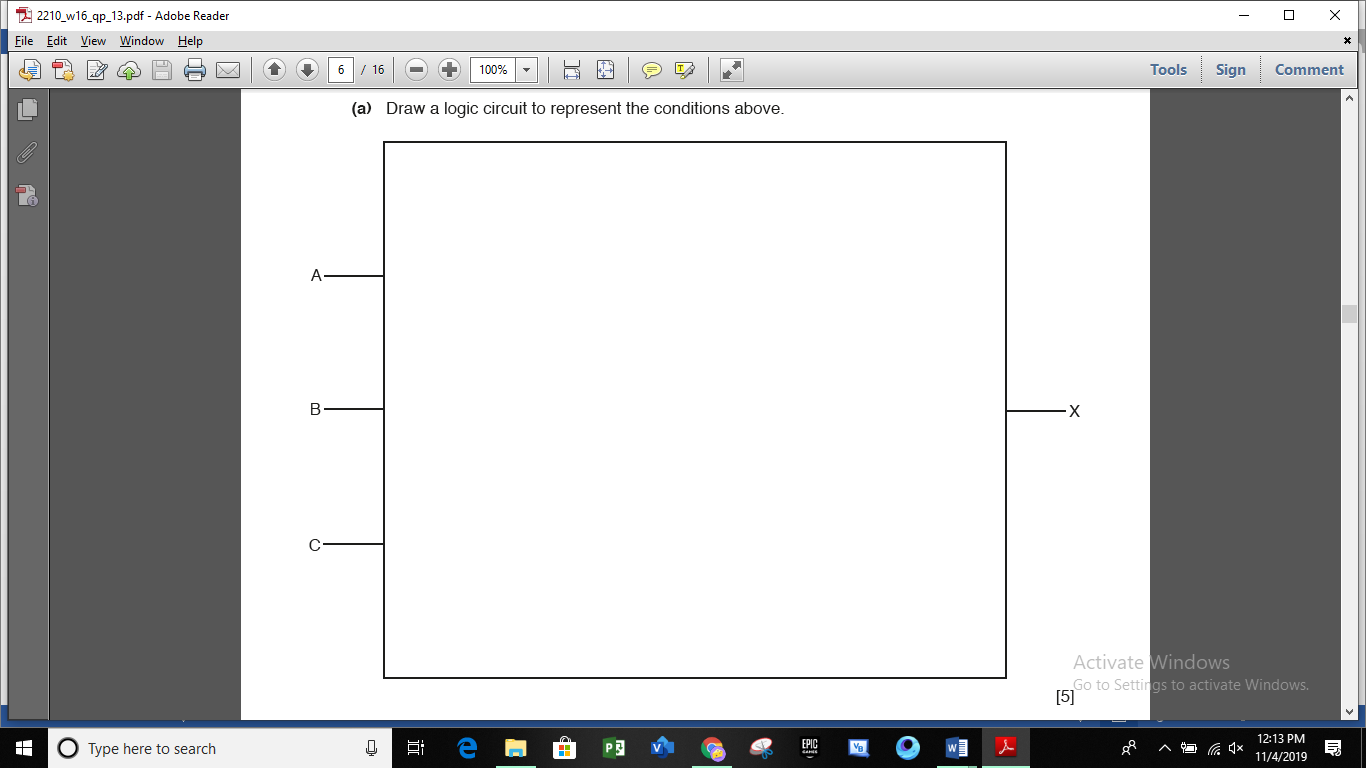


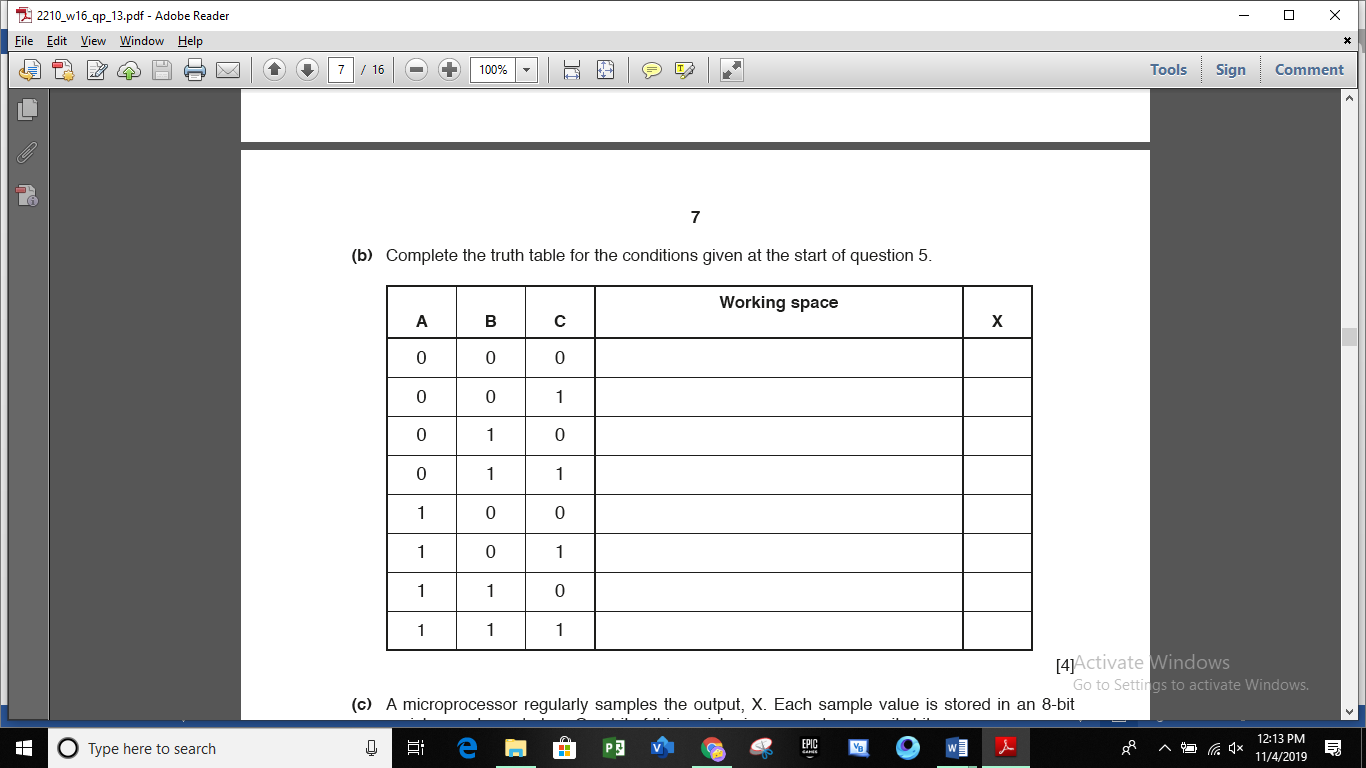




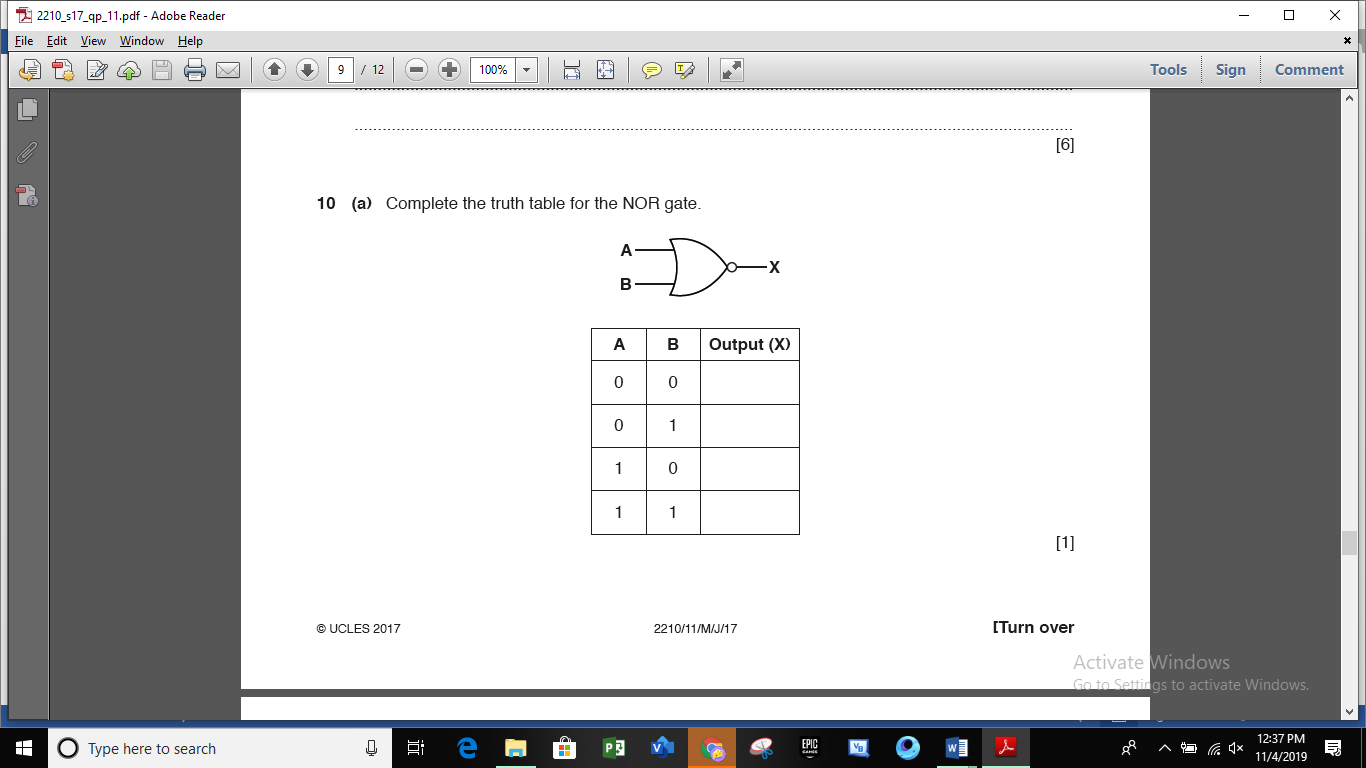
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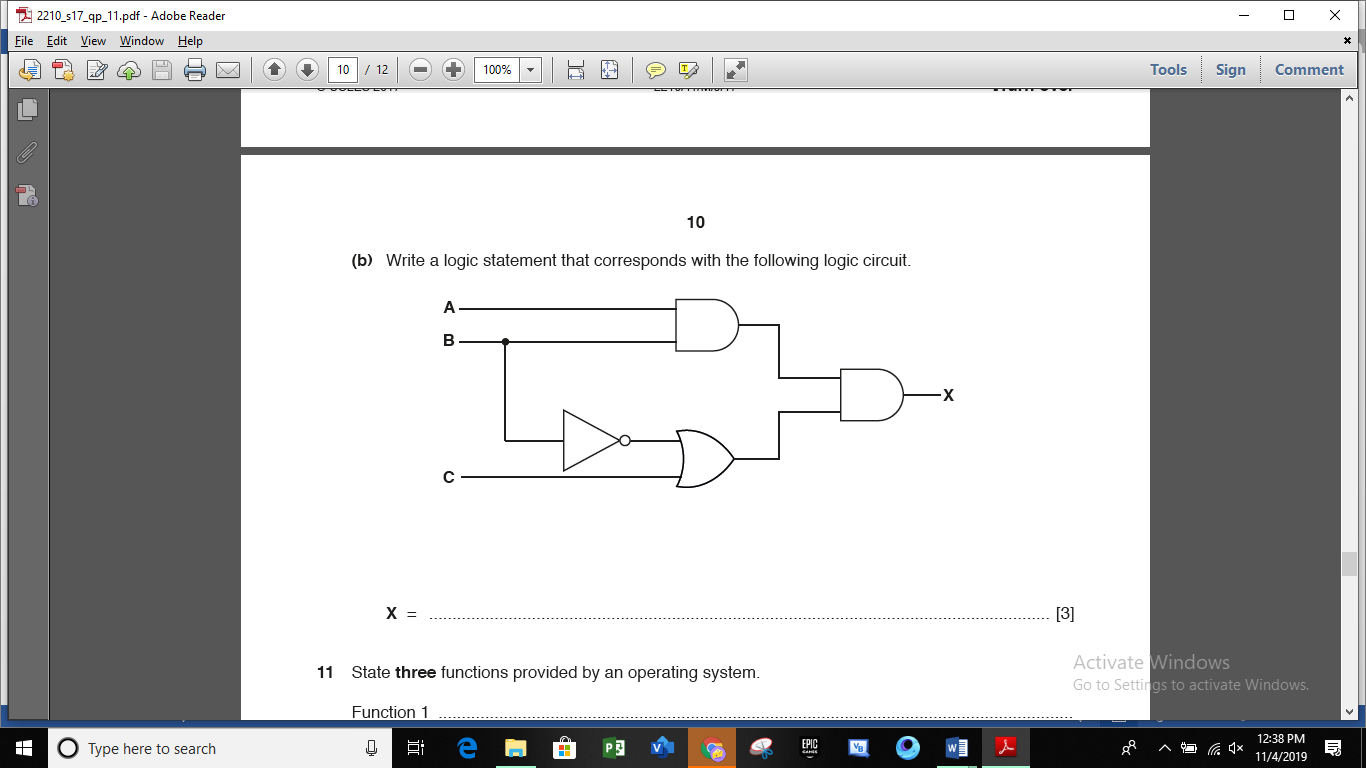




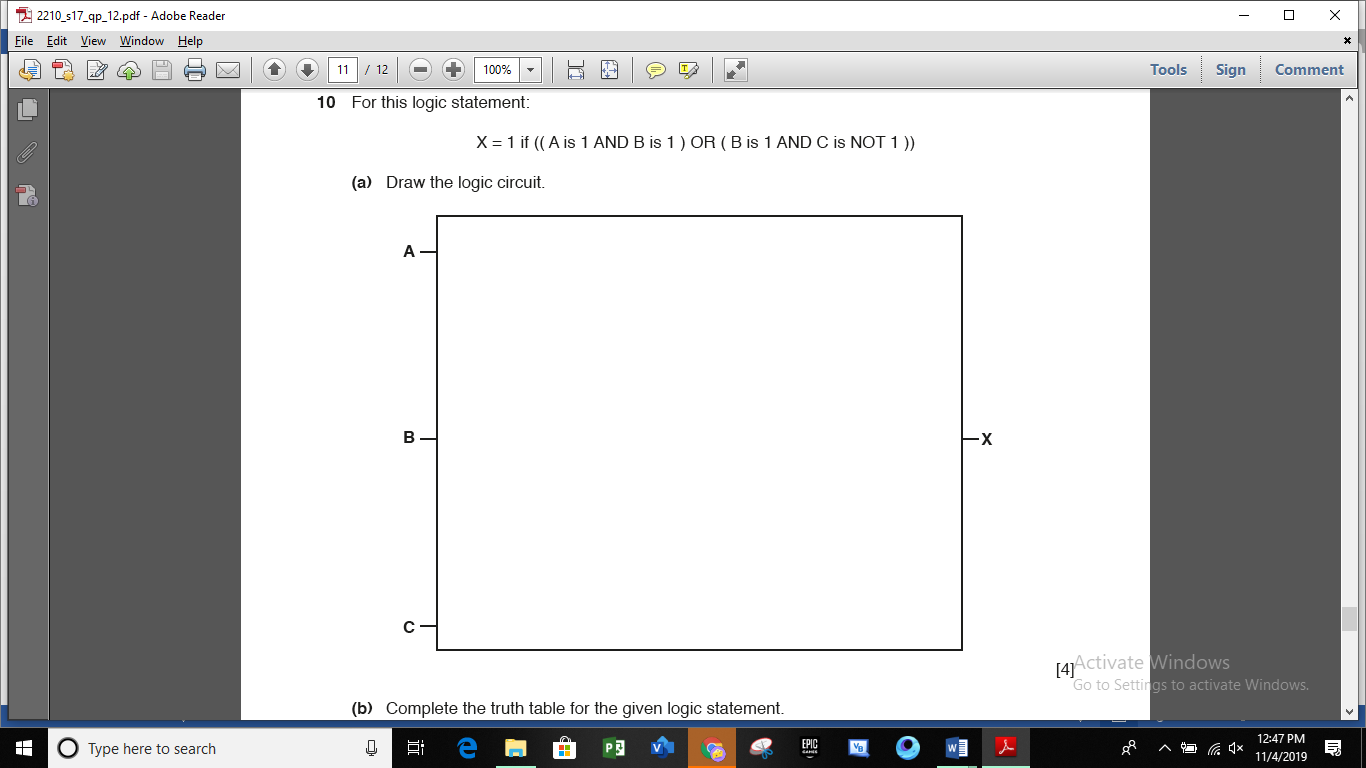


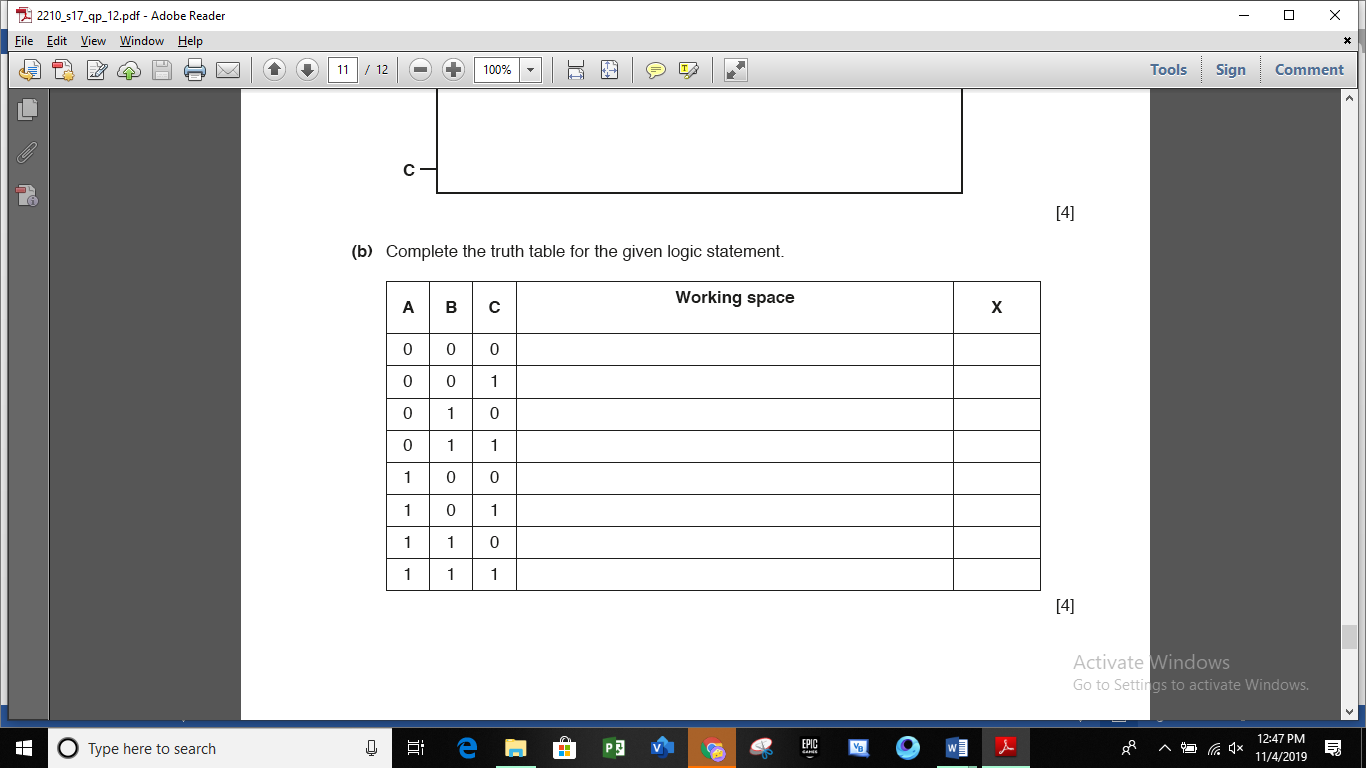
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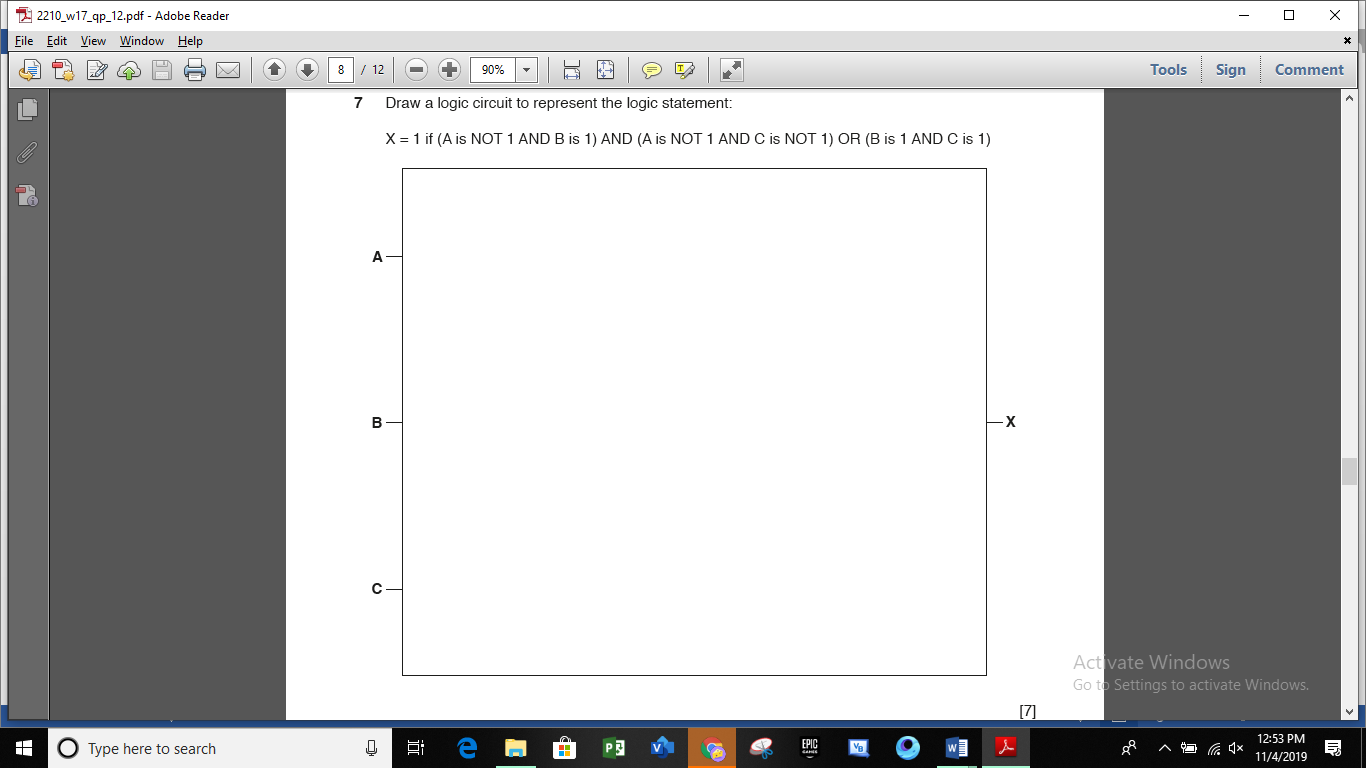


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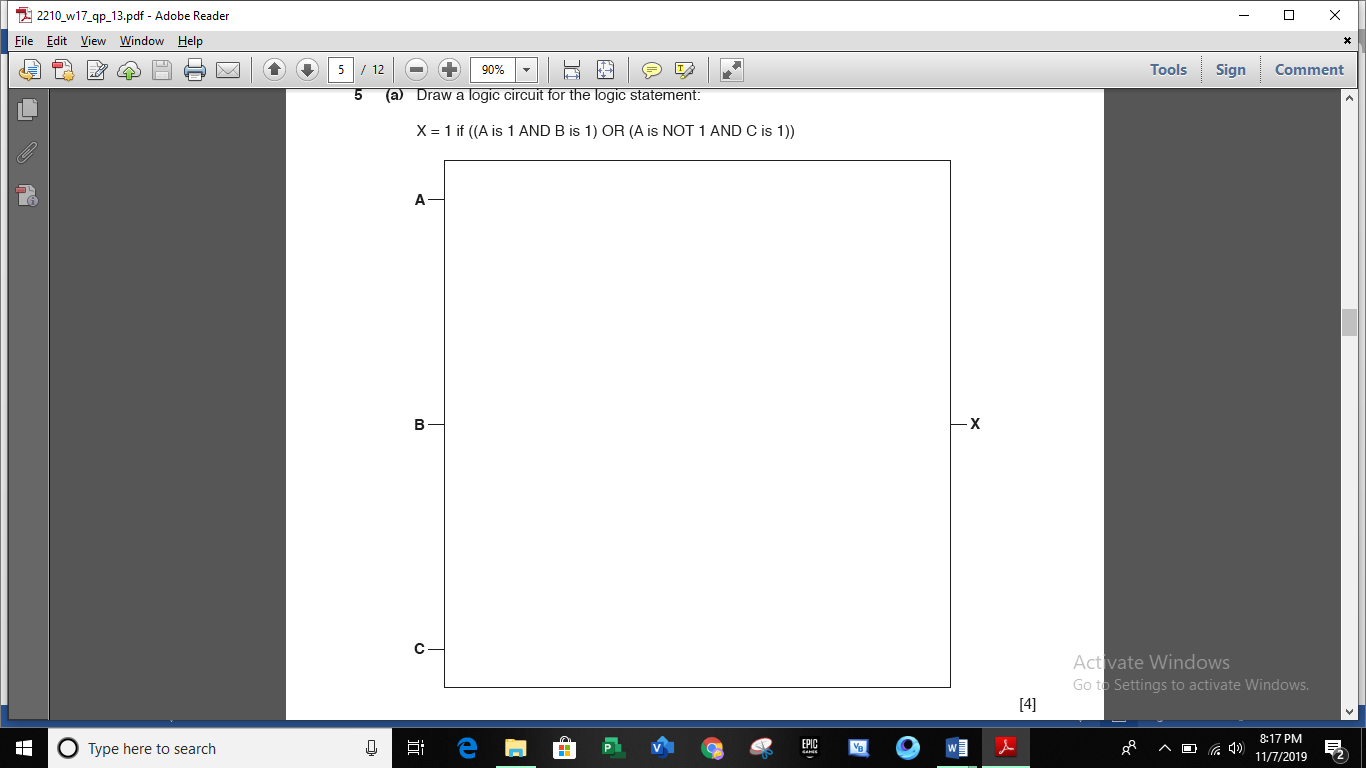


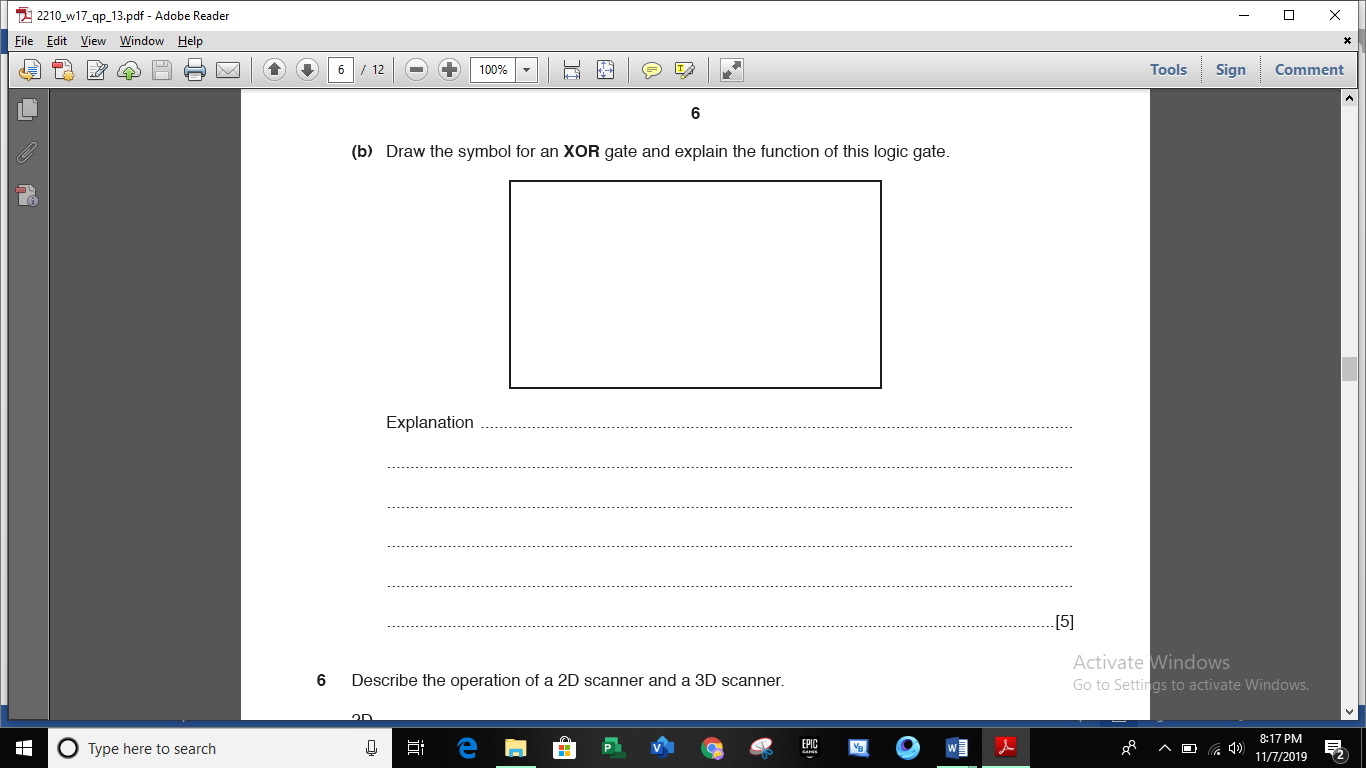


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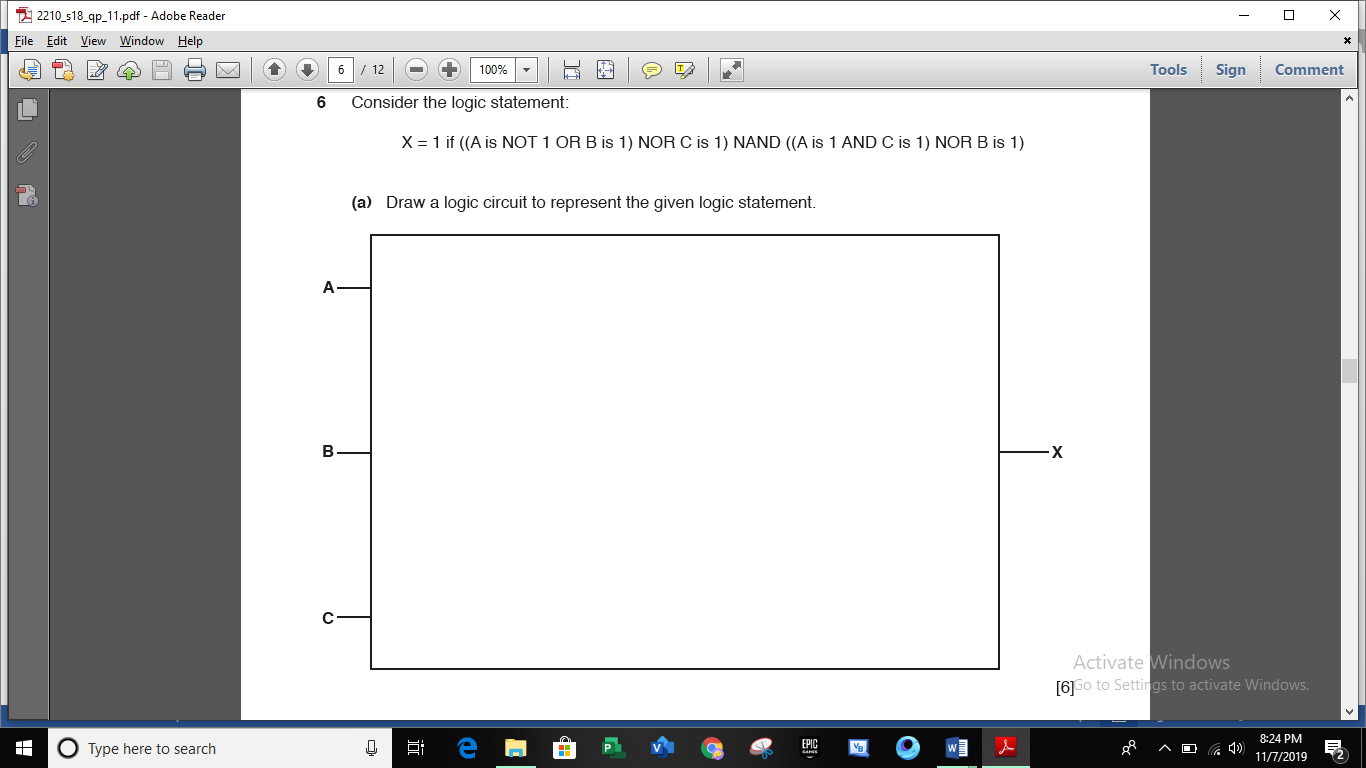


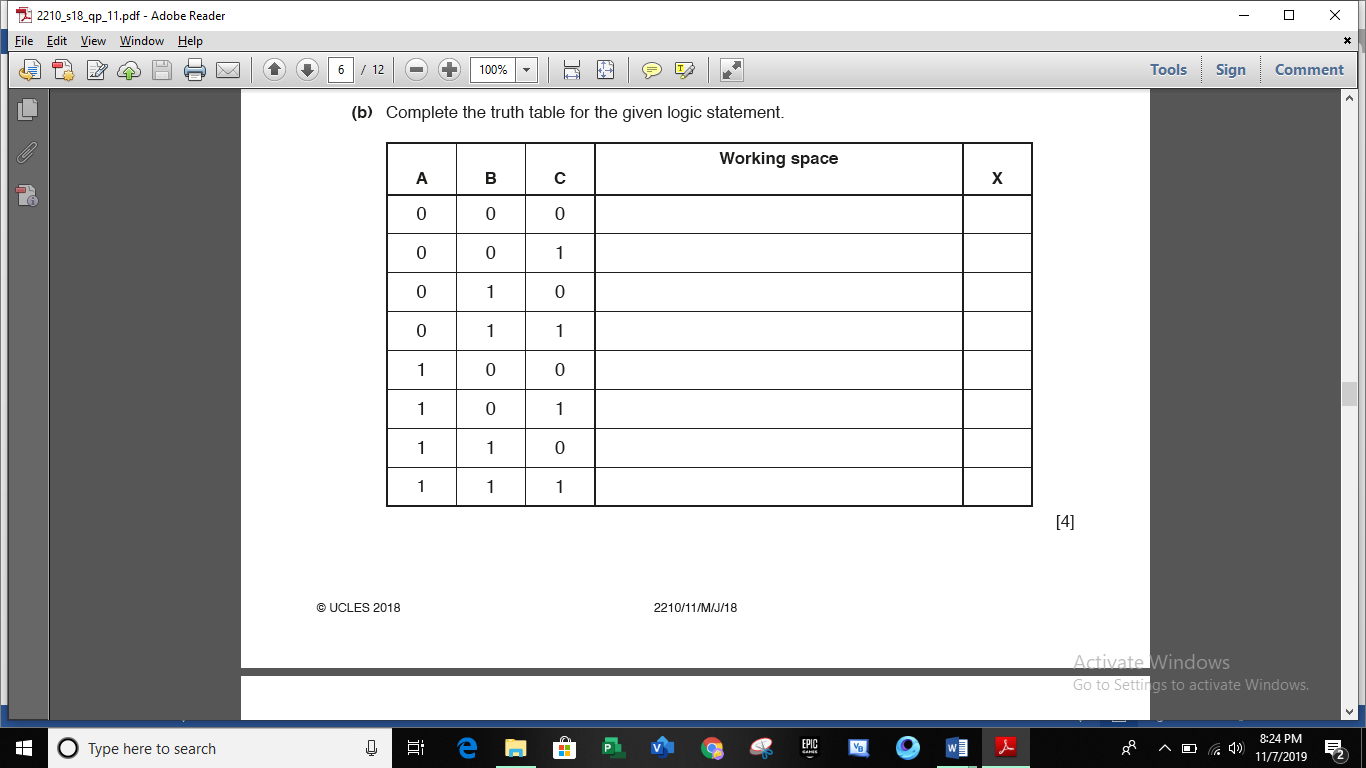
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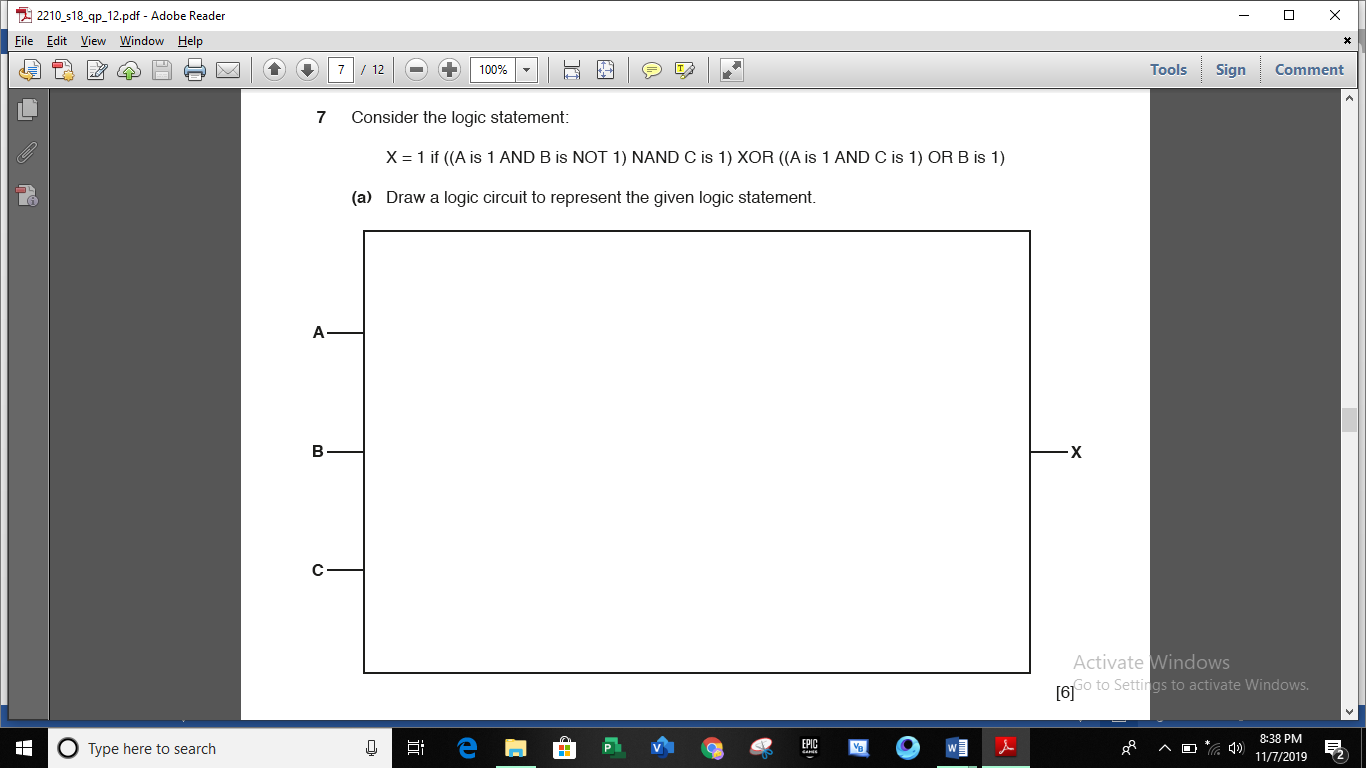


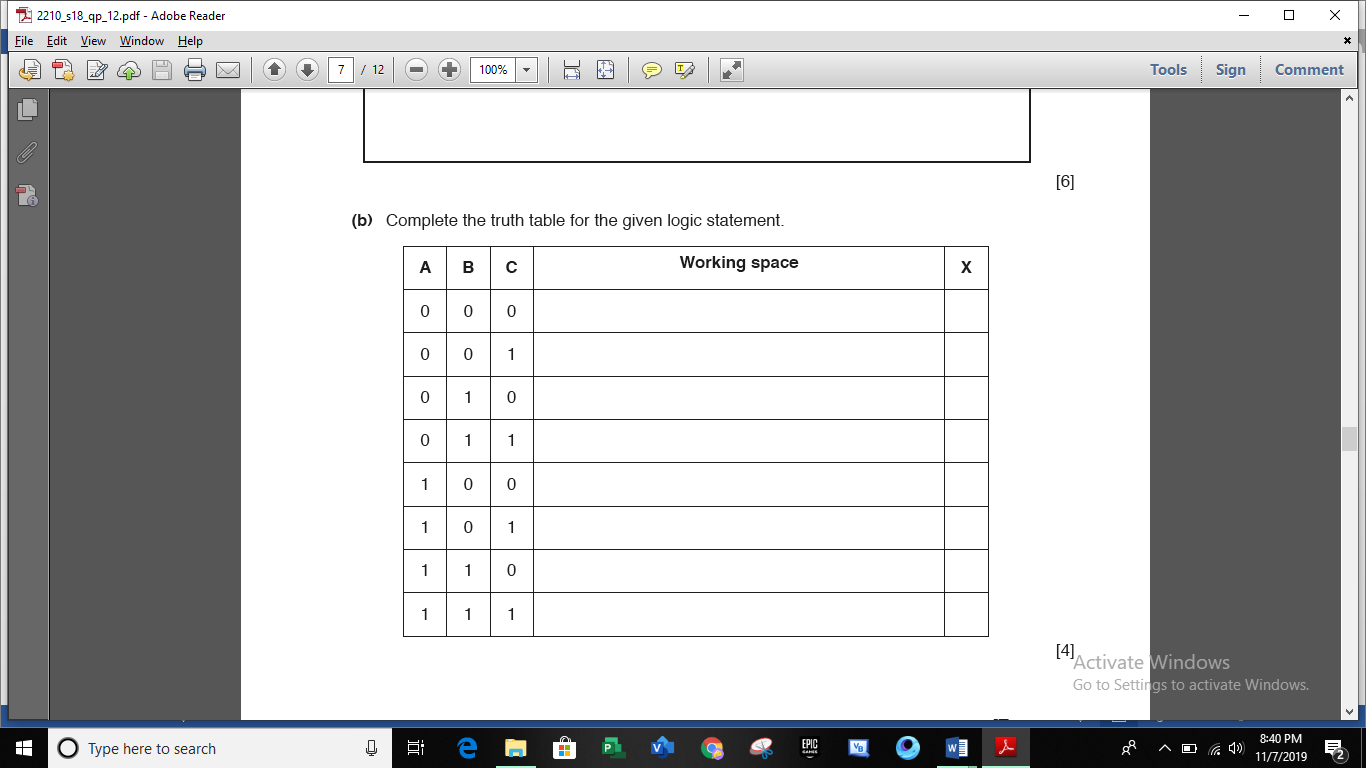
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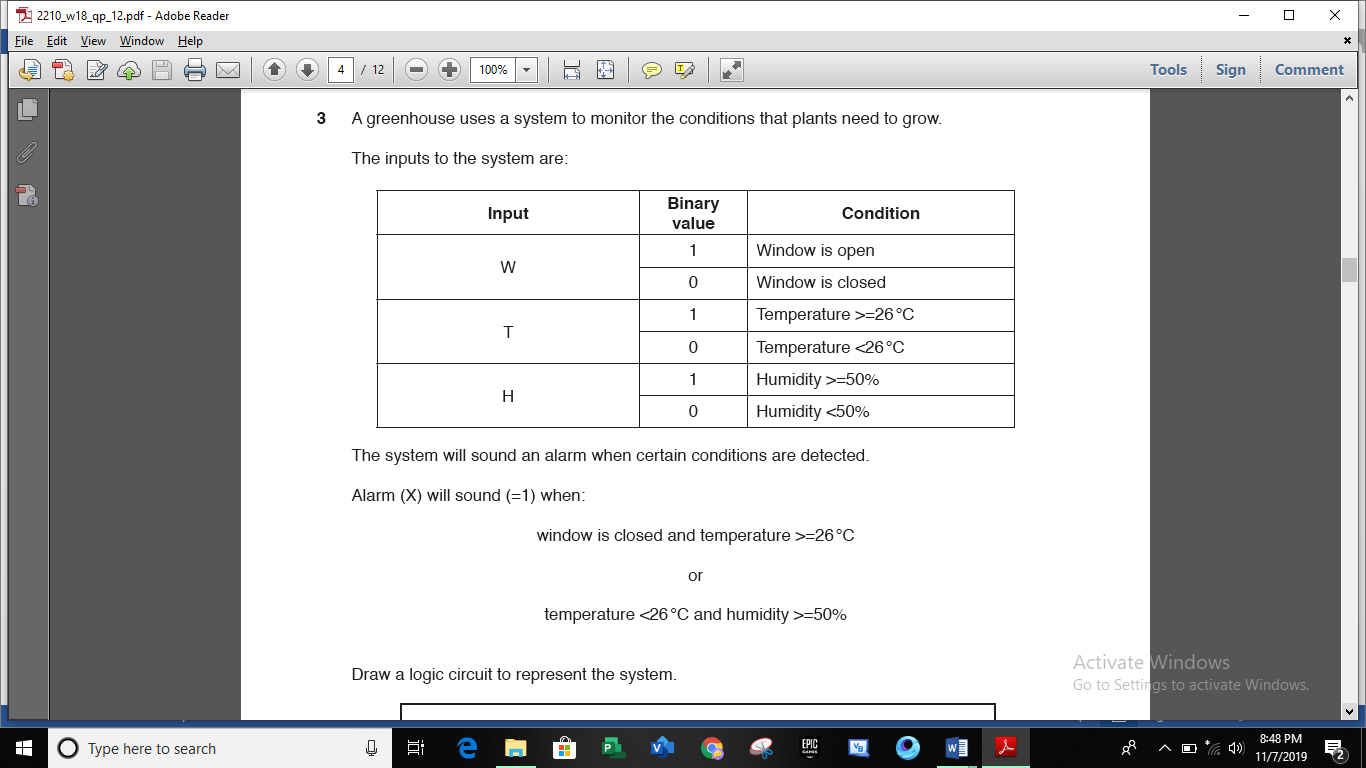


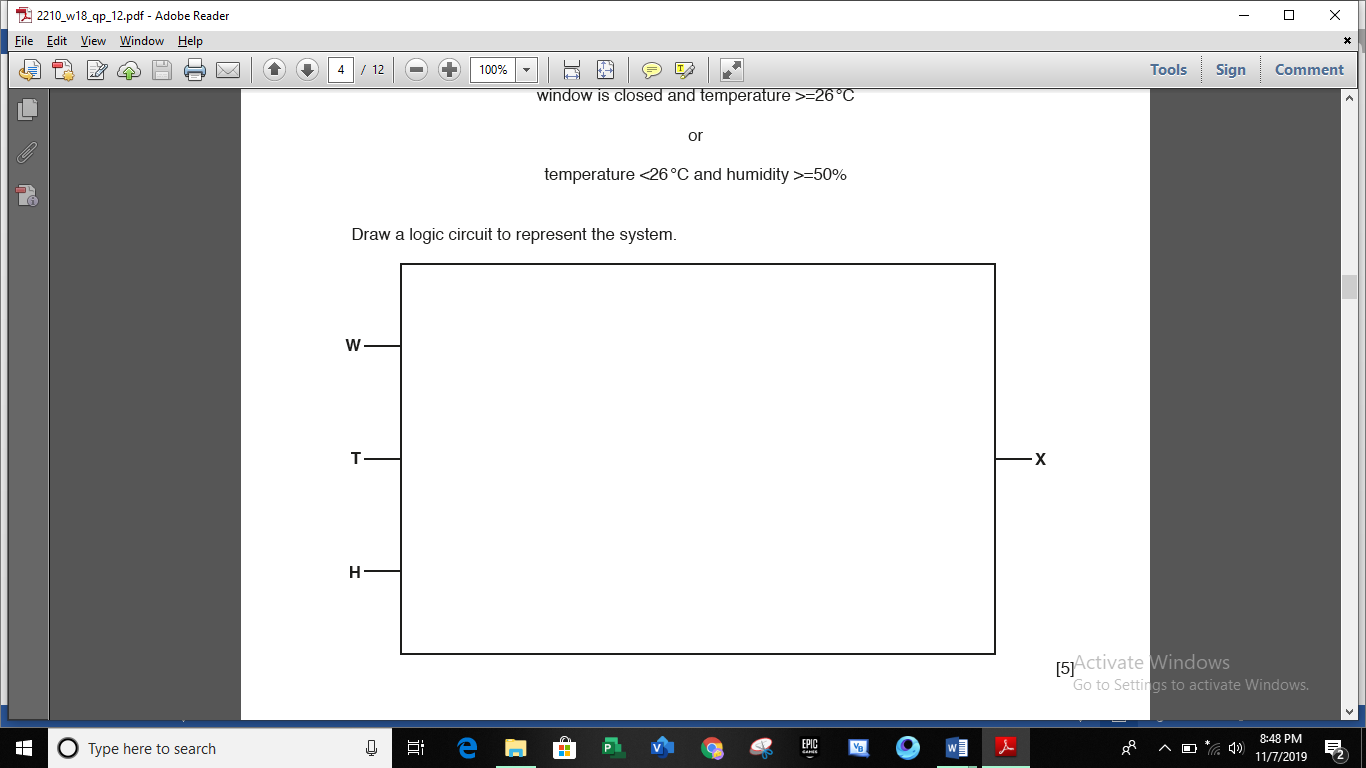
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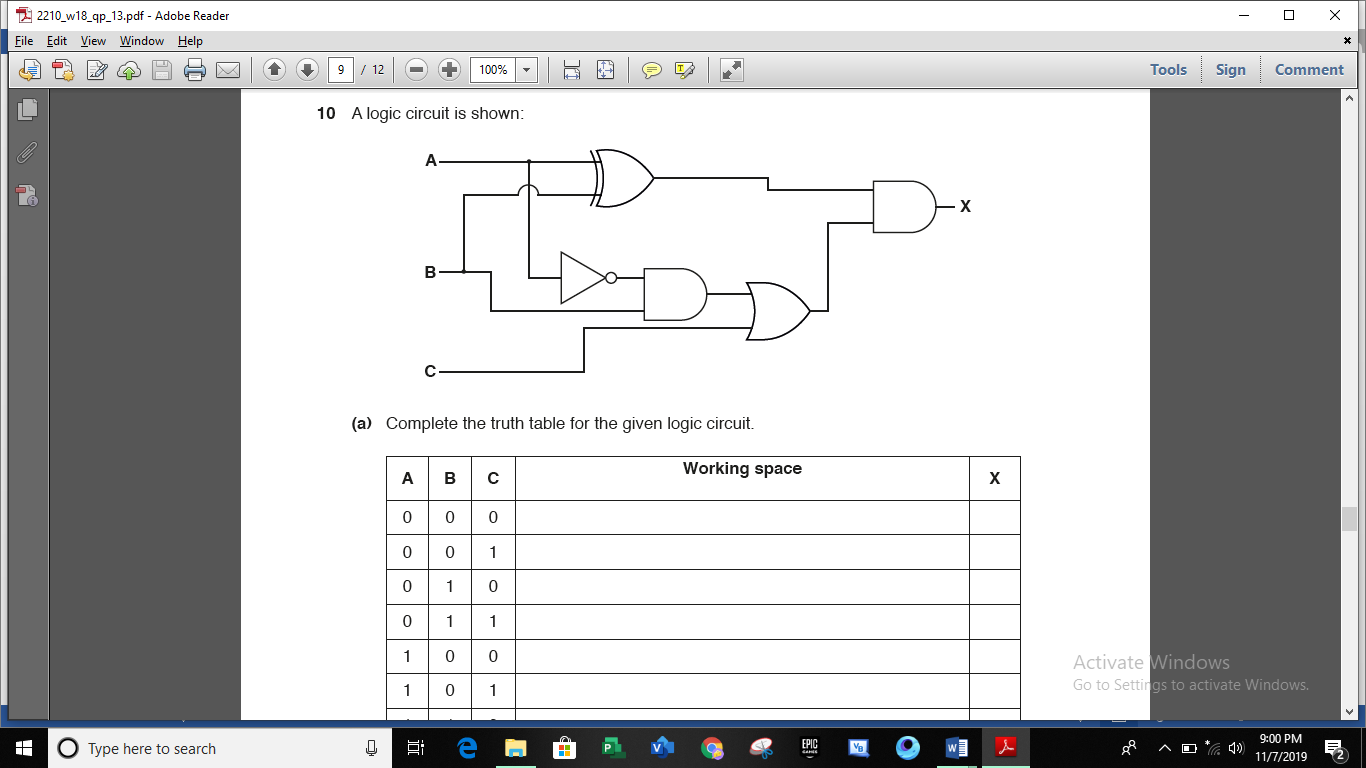


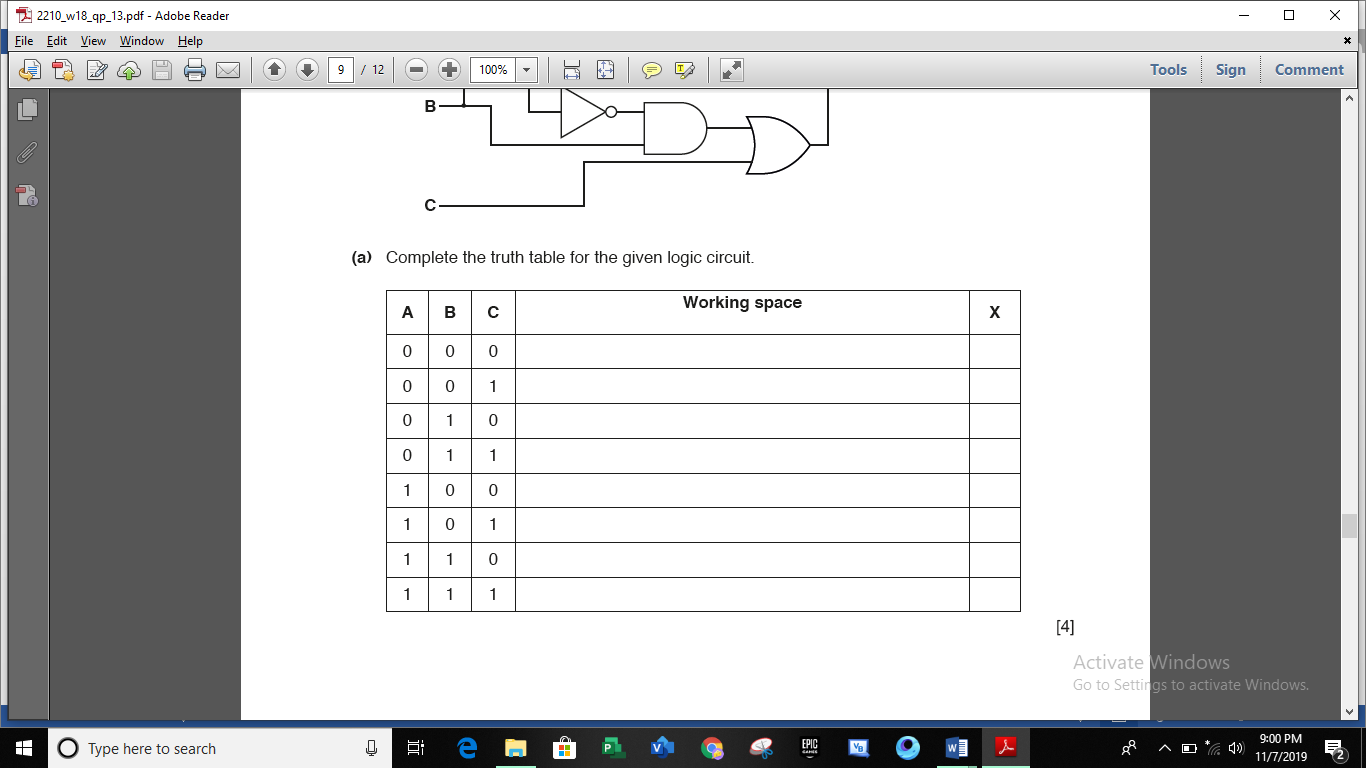
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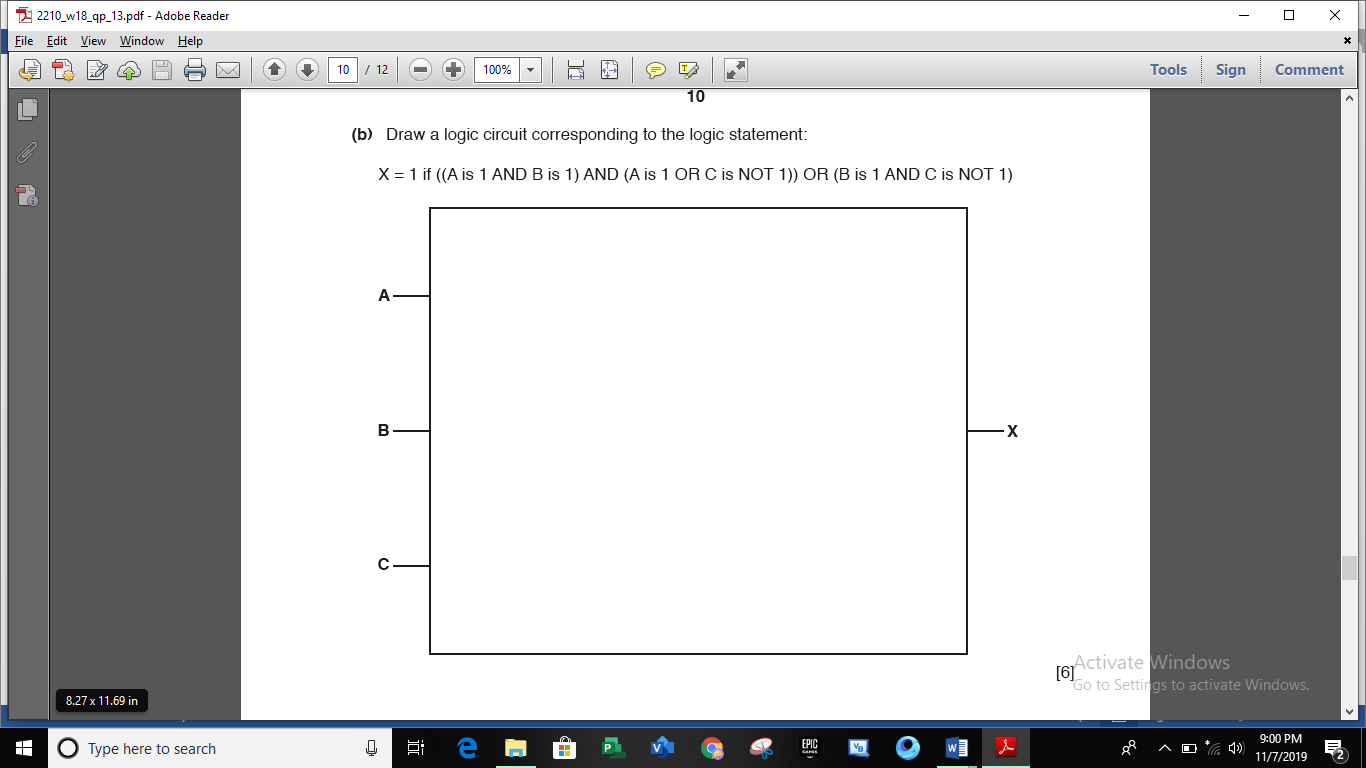




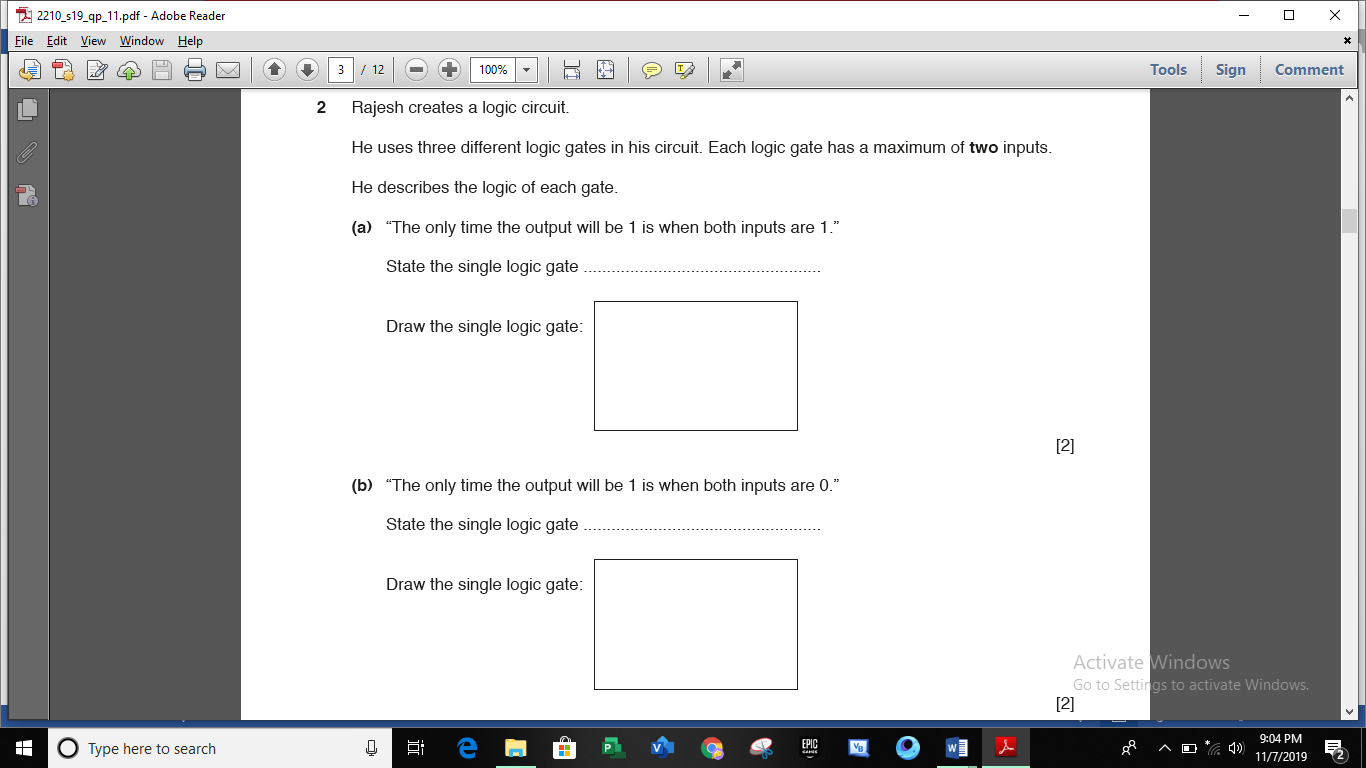
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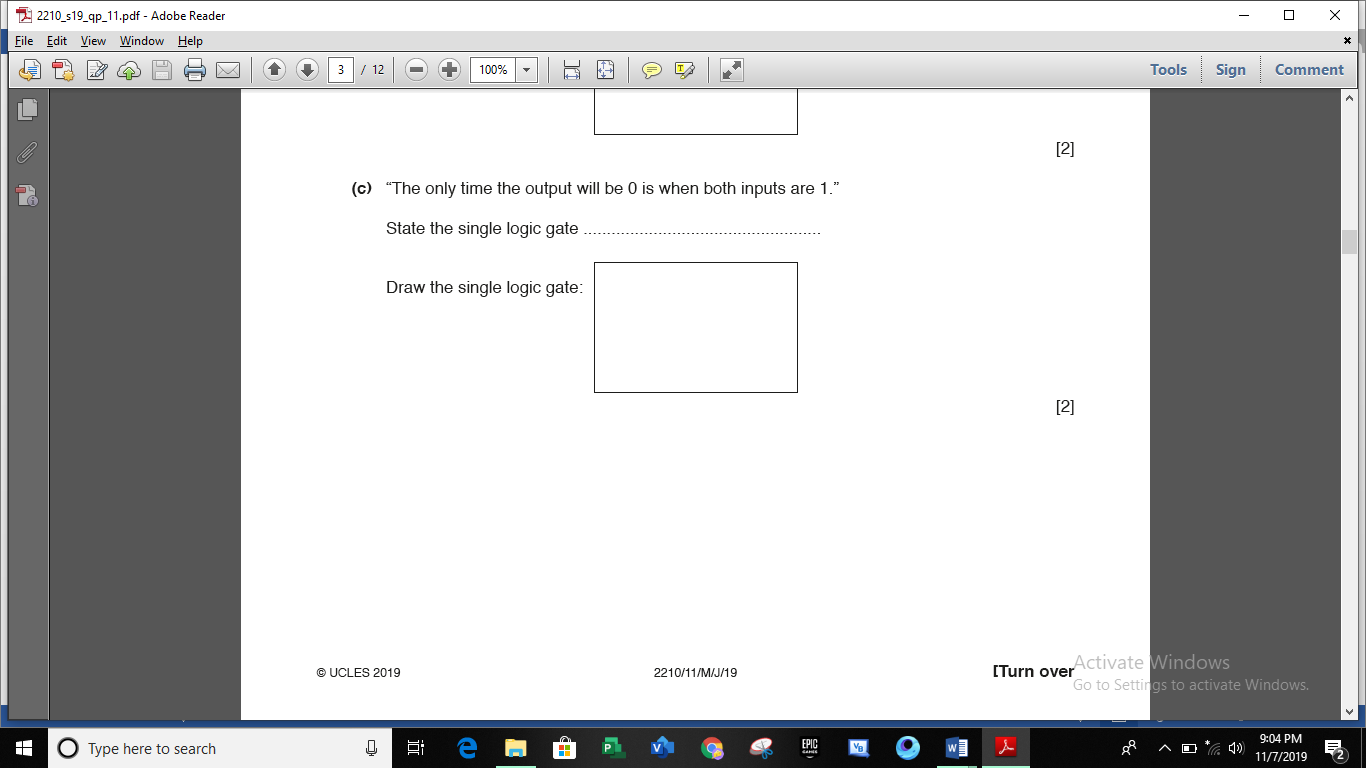




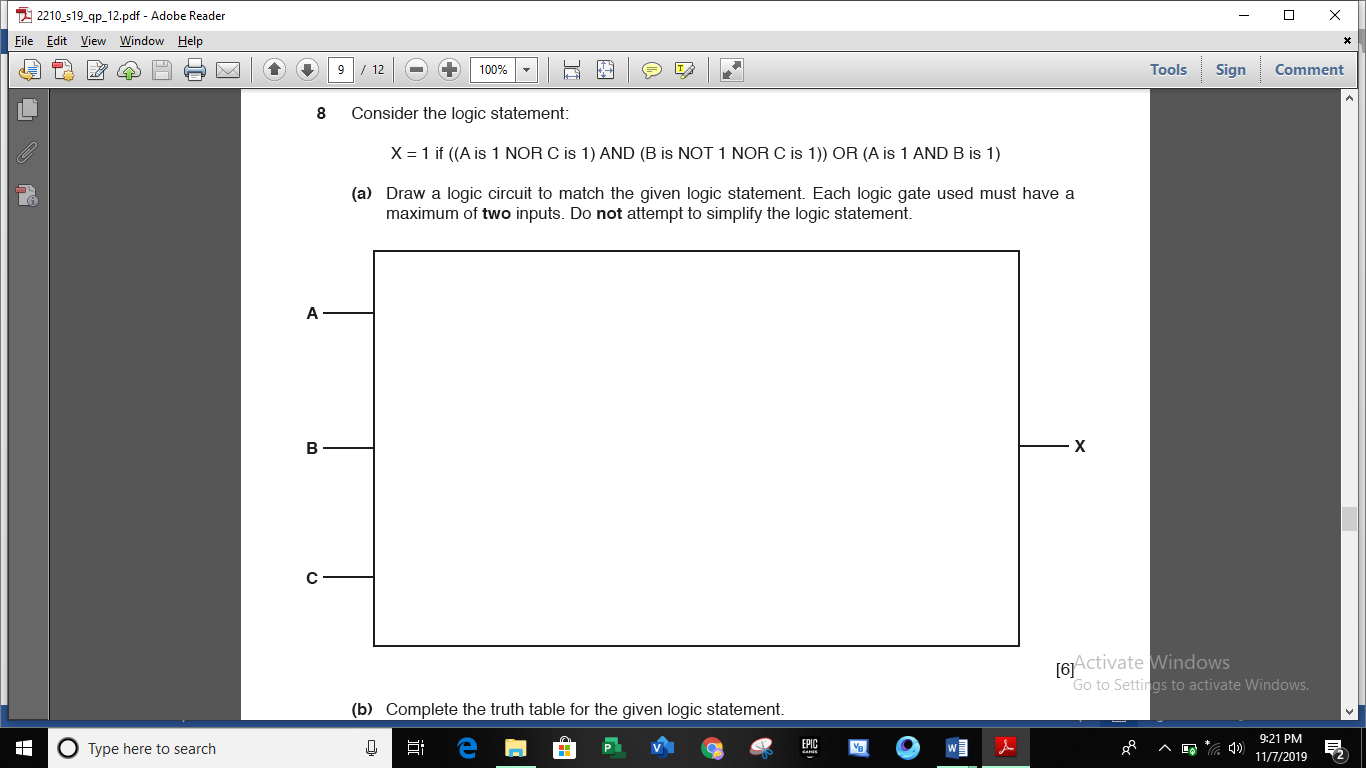


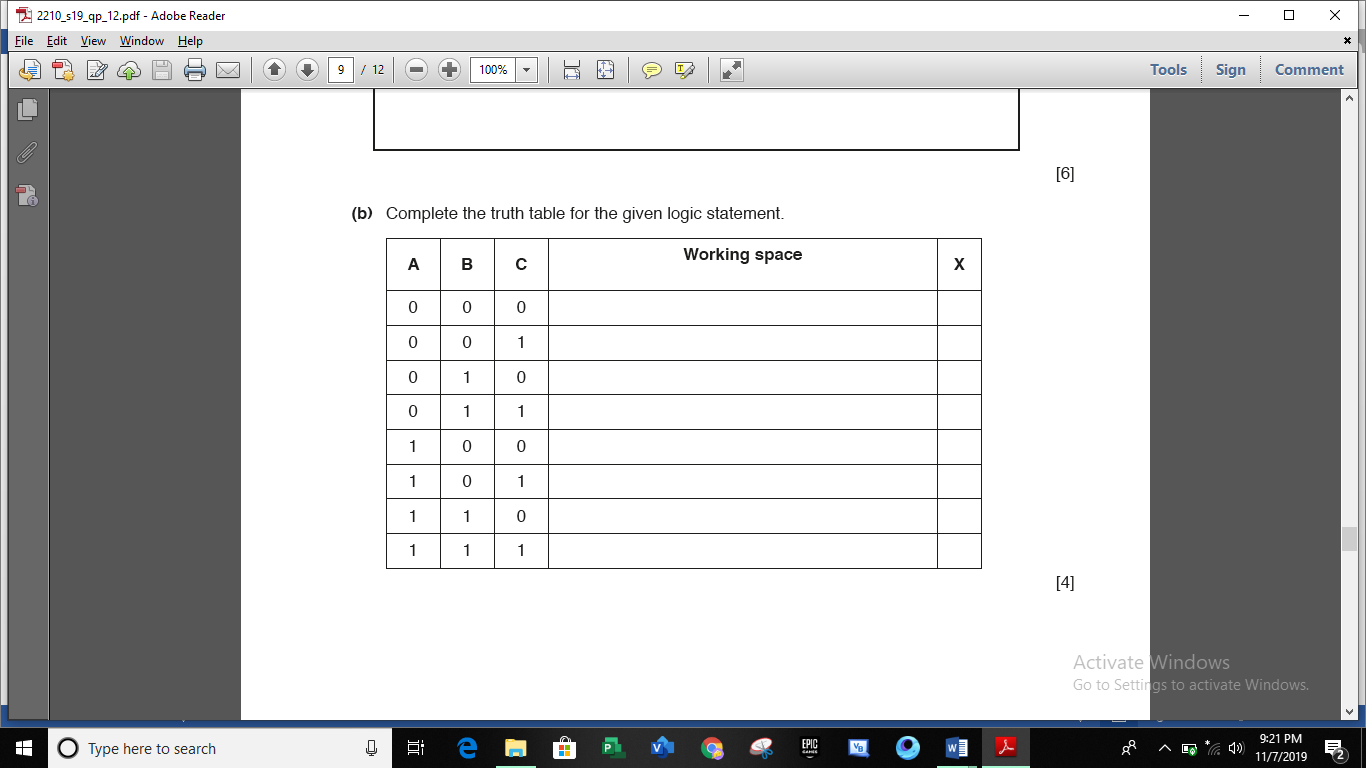
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2210\_s19\_qp12





2210\_w19\_qp12

2210\_w19\_qp13